# Sampling parameter

| Command | Data type | Description | PS | AX | RW | Range |
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| M_SSEC_SMP_PRM_TRIGGER | T_SHORT | Sampling trigger | -- | -- | RW | 0,1,9 |
| M_SSEC_SMP_PRM_SAMP_RATE | T_SHORT | Sampling cycle | -- | -- | RW | 0x01 to 0xFF |
| M_SSEC_SMP_PRM_SAMP_NOS | T_SHORT | The number of pieces of sampling data | -- | -- | RW | 0 to 8 |
| M_SSEC_SMP_PRM_BUFF_ADDR | T_LONG | Sampling data buffer address | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_BUFF_SIZE | T_LONG | Sampling data buffer size | -- | -- | RW | 0 to Size |
| M_SSEC_SMP_PRM_SAMP_ADDR1 | T_LONG | Sampling address #1 (Compatible with M625) | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_SAMP_ADDR2 | T_LONG | Sampling address #2 (Compatible with M625) | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_SAMP_ADDR3 | T_LONG | Sampling address #3 (Compatible with M625) | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_SAMP_ADDR4 | T_LONG | Sampling address #4 (Compatible with M625) | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_SAMP_ADDR5 | T_LONG | Sampling address #5 (Compatible with M625) | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_SAMP_ADDR6 | T_LONG | Sampling address #6 (Compatible with M625) | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_SAMP_ADDR7 | T_LONG | Sampling address #7 (Compatible with M625) | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_SAMP_ADDR8 | T_LONG | Sampling address #8 (Compatible with M625) | -- | -- | RW | 0 to Address |
| M_SSEC_SMP_PRM_SAMP_EX_ADDR1 | T_LONG | Sampling address #1 | -- | -- | RW | 0 to Address or Symbol (Within 16 letters) |
| M_SSEC_SMP_PRM_SAMP_EX_ADDR2 | T_LONG | Sampling address #2 | -- | -- | RW | 0 to Address or Symbol (Within 16 letters) |
| M_SSEC_SMP_PRM_SAMP_EX_ADDR3 | T_LONG | Sampling address #3 | -- | -- | RW | 0 to Address or Symbol (Within 16 letters) |
| M_SSEC_SMP_PRM_SAMP_EX_ADDR4 | T_LONG | Sampling address #4 | -- | -- | RW | 0 to Address or Symbol (Within 16 letters) |

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| M_SSEC_SMP_PRM_SAMP_EX_ADDR5 | T_LONG | Sampling address #5 | -- | -- | RW | 0 to Address or Symbol (Within 16 letters) |
| M_SSEC_SMP_PRM_SAMP_EX_ADDR6 | T_LONG | Sampling address #6 | -- | -- | RW | 0 to Address or Symbol (Within 16 letters) |
| M_SSEC_SMP_PRM_SAMP_EX_ADDR7 | T_LONG | Sampling address #7 | -- | -- | RW | 0 to Address or Symbol (Within 16 letters) |
| M_SSEC_SMP_PRM_SAMP_EX_ADDR8 | T_LONG | Sampling address #8 | -- | -- | RW | 0 to Address or Symbol (Within 16 letters) |
| M_SSEC_SMP_PRM_SAMP_AT_ADDR1 | T_LONG | Sampling address #1 for AT | -- | -- | W | 0 to 0xFFFFFFFF(Special data) |
| M_SSEC_SMP_PRM_SAMP_AT_ADDR2 | T_LONG | Sampling address #2 for AT | -- | -- | W | 0 to 0xFFFFFFFF(Special data) |
| M_SSEC_SMP_PRM_SAMP_AT_ADDR3 | T_LONG | Sampling address #3 for AT | -- | -- | W | 0 to 0xFFFFFFFF(Special data) |
| M_SSEC_SMP_PRM_SAMP_AT_ADDR4 | T_LONG | Sampling address #4 for AT | -- | -- | W | 0 to 0xFFFFFFFF(Special data) |
| M_SSEC_SMP_PRM_SAMP_AT_ADDR5 | T_LONG | Sampling address #5 for AT | -- | -- | W | 0 to 0xFFFFFFFF(Special data) |
| M_SSEC_SMP_PRM_SAMP_AT_ADDR6 | T_LONG | Sampling address #6 for AT | -- | -- | W | 0 to 0xFFFFFFFF(Special data) |
| M_SSEC_SMP_PRM_SAMP_AT_ADDR7 | T_LONG | Sampling address #7 for AT | -- | -- | W | 0 to 0xFFFFFFFF(Special data) |
| M_SSEC_SMP_PRM_SAMP_AT_ADDR8 | T_LONG | Sampling address #8 for AT | -- | -- | W | 0 to 0xFFFFFFFF(Special data) |
| M_SSEC_SMP_PRM_TRGMOD | T_LONG | Buffer designation (0: Internal memory, 1: Cassette memory) | -- | -- | RW | 0,1(1 is available only for M65V) |
| M_SSEC_SMP_PRM_BUFF_CAP | T_LONG | Buffer capacity (1024*(Setting value+1) byte) | -- | -- | RW | 0 to n |
| M_SSEC_SMP_PRM_START_COND | T_LONG | Start condition | -- | -- | RW | 0 to 4 |
| M_SSEC_SMP_PRM_PROC_FORM | T_LONG | Process format (0: 1-shot, 1: Repeat, 2: Ring buffer) | -- | -- | RW | 0 to 2 |
| M_SSEC_SMP_PRM_END_COND | T_LONG | End condition | -- | -- | RW | 0 to 4 |
| M_SSEC_SMP_PRM_VAR_NUM | T_LONG | Variable No. (0: #1299, Other than 0: Common variable) | -- | -- | RW | 0 to 999 |
| M_SSEC_SMP_PRM_PLC_DEV | T_STR | PLC device | -- | -- | RW | Within 6 letters |
| M_SSEC_SMP_PRM_COND_ADDR | T_LONG | Condition address | -- | -- | RW | long range |
| M_SSEC_SMP_PRM_COND_DATA | T_LONG | Condition data | -- | -- | RW | long range |
| M_SSEC_SMP_PRM_COND_MASK | T_LONG | Condition data mask | -- | -- | RW | long range |
| M_SSEC_SMP_PRM_SAMP_OUT | T_LONG | Sampling data output type | -- | -- | RW | 0 to 2 |
| M_SSEC_SMP_PRM_SAMP_MAX_DATA | T_LONG | The maximum number of pieces of sampling data | -- | -- | R | 0 to 1310720 |
| M_SSEC_SMP_PRM_SAMPDAT_UNIT1 | T_STR | Sampling data output unit #1 | -- | -- | RW | 0(Blank),B to E,S |

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| M_SSEC_SMP_PRM_SAMPDAT_UNIT2 | T_STR | Sampling data output unit #2 | -- | -- | RW | 0(Blank),B to E,S |
| M_SSEC_SMP_PRM_SAMPDAT_UNIT3 | T_STR | Sampling data output unit #3 | -- | -- | RW | 0(Blank),B to E,S |
| M_SSEC_SMP_PRM_SAMPDAT_UNIT4 | T_STR | Sampling data output unit #4 | -- | -- | RW | 0(Blank),B to E,S |
| M_SSEC_SMP_PRM_SAMPDAT_UNIT5 | T_STR | Sampling data output unit #5 | -- | -- | RW | 0(Blank),B to E,S |
| M_SSEC_SMP_PRM_SAMPDAT_UNIT6 | T_STR | Sampling data output unit #6 | -- | -- | RW | 0(Blank),B to E,S |
| M_SSEC_SMP_PRM_SAMPDAT_UNIT7 | T_STR | Sampling data output unit #7 | -- | -- | RW | 0(Blank),B to E,S |
| M_SSEC_SMP_PRM_SAMPDAT_UNIT8 | T_STR | Sampling data output unit #8 | -- | -- | RW | 0(Blank),B to E,S |
| M_SSEC_SMP_PRM_AT_SMP_STS | T_LONG | Sampling status (For auto tuning) | -- | -- | R | -- |
| M_SSEC_SMP_PRM_AT_DELAY | T_LONG | Sampling delay time (For auto tuning) | -- | -- | RW | 0 to 0x7FFFFFFF |
| M_SSEC_SMP_PRM_AT_START_REQ | T_LONG | Cycle start request (For auto tuning) | -- | -- | W | 0 to 0xFFFFFFFF |
| M_SSEC_SMP_PRM_AT_START_LOCK | T_LONG | Cycle start lock (For auto tuning) | -- | -- | RW | 0 to 1 |
| M_SSEC_SMP_PRM_AT_SAMP_NOS | T_LONG | The number of pieces of sampling data (For auto tuning) | -- | -- | R | -- |
| M_SSEC_SMP_PRM_AT_OP_CHK | T_LONG | OP check information (For auto tuning) | -- | -- | RW | 0 to 2 |