Parameter

Command	Description
SECT_MCPARA_POS_SW2_AXIS	axis
SECT_MCPARA_POS_SW2_DOG1	dog1
SECT_MCPARA_POS_SW2_DOG2	dog2
SECT_MCPARA_POS_SW3_AXIS	axis
SECT_MCPARA_POS_SW3_DOG1	dog1
SECT_MCPARA_POS_SW3_DOG2	dog2
SECT_MCPARA_POS_SW4_AXIS	axis
SECT_MCPARA_POS_SW4_DOG1	dog1
SECT_MCPARA_POS_SW4_DOG2	dog2
SECT_MCPARA_POS_SW5_AXIS	axis
SECT_MCPARA_POS_SW5_DOG1	dog1
SECT_MCPARA_POS_SW5_DOG2	dog2
SECT_MCPARA_POS_SW6_AXIS	axis
SECT_MCPARA_POS_SW6_DOG1	dog1
SECT_MCPARA_POS_SW6_DOG2	dog2
SECT_MCPARA_POS_SW7_AXIS	axis
SECT_MCPARA_POS_SW7_DOG1	dog1
SECT_MCPARA_POS_SW7_DOG2	dog2
SECT_MCPARA_POS_SW8_AXIS	axis
SECT_MCPARA_POS_SW8_DOG1	dog1
SECT_MCPARA_POS_SW8_DOG2	dog2
SECT_MCPARA_MMAC	Mmac
SECT_MCPARA_SMAC	Smac
SECT_MCPARA_TMAC	Tmac
SECT_MCPARA_M2MAC	M2mac
SECT_MCPARA_M_INCH	M_inch
SECT_MCPARA_FIX_P	fix_P
SECT_MCPARA_EDLK_C	edlk_C
SECT_MCPARA_PINC	Pinc
SECT_MCPARA_DPRINT	DPRINT

SECT_MCPARA_OFSFIX	ofsfix
SECT_MCPARA_TMIRON	Tmiron
SECT_MCPARA_G96_G0	G96_G0
SECT_MCPARA_RADIUS	radius
SECT_MCPARA_T1DIGIT	T1digit
SECT_MCPARA_TLNO	TLno.
SECT_MCPARA_TRESET	Treset
SECT_MCPARA_TMOVE	Tmove
SECT_MCPARA_RSTINT	rstint
SECT_MCPARA_I_ABS	I_abs
SECT_MCPARA_H_ACDC	H_acdc
SECT_MCPARA_G3OSL	G30SL
SECT_MCPARA_INPOS	inpos
SECT_MCPARA_TLM	tlm
SECT_MCPARA_LANG	lang
SECT_MCPARA_MIRR_A	mirr_A
SECT_MCPARA_SP_1	sp_1
SECT_MCPARA_SP_2	_2
SECT_MCPARA_SP_3	_3
SECT_MCPARA_SP_4	_4
SECT_MCPARA_SP_5	_5
SECT_MCPARA_SP_6	_6
SECT_MCPARA_MSTSYN	mstsyn
SECT_MCPARA_TCOM	Tcom
SECT_MCPARA_SYNCCH	syncch
SECT_MCPARA_DSPAX	dspax
SECT_MCPARA_CRSMAN	crsman
SECT_MCPARA_OTSYS	otsys
SECT_MCPARA_TGSMAX	TGSmax
SECT_MCPARA_H1_PNO	H1_pno
SECT_MCPARA_H2_PNO	H2_pno
SECT_MCPARA_H3_PNO	H3_pno
SECT_MCPARA_STATIO	statio

SECT_MCPARA_SIZE_I	size-i
SECT_MCPARA_SIZE_O	size-o
SECT_MCPARA_LENGTH	length
SECT_MCPARA_B_RATE	b-rate
SECT_MCPARA_S_BIT	s-bit
SECT_MCPARA_PARITY	parity
SECT_MCPARA_EVEN	even
SECT_MCPARA_TOUT_I	Tout-i
SECT_MCPARA_TOUT_O	Tout-o
SECT_MCPARA_SIOBUS	siobus
SECT_MCPARA_CMACDB	cmacdb
SECT_MCPARA_GBTEST	GBtest
SECT_MCPARA_COOL_T	COOL_t
SECT_MCPARA_SBSSYS	SBSsys
SECT_MCPARA_SP2NAME	SP2name
SECT_MCPARA_SP_7	sp_7
SECT_MCPARA_SP_8	sp_8
SECT_MCPARA_SUNIT	sunit
SECT_MCPARA_TP1SIT	tp1sit
SECT_MCPARA_SP1OV1	tp1ov1
SECT_MCPARA_TP1OV2	tp1ov2
SECT_MCPARA_TP1OV3	tp1ov3
SECT_MCPARA_TP2SIT	tp2sit
SECT_MCPARA_TP2OV1	tp2ov1
SECT_MCPARA_TP2OV2	tp2ov2
SECT_MCPARA_TP2OV3	tp2ov3
SECT_MCPARA_NSRCRS	nsrcrs
SECT_MCPARA_G59RST	G59rst
SECT_MCPARA_G96PRM	G96rpm
SECT_MCPARA_NO_DSP_SYS	no_dsp_sys
SECT_MCPARA_HSPRO	hspro
SECT_MCPARA_TAP_OVR	TAPovr
SECT_MCPARA_ITF3_SPEC	ITF3_spec

SECT_MCPARA_M_TYPE	M_type
SECT_MCPARA_S_MODE	S_mode
SECT_MCPARA_T_MODE	T_mode
SECT_MCPARA_M2_MODE	M2_mode
SECT_MCPARA_M031_000	M031-000
SECT_MCPARA_M063_032	M063-032
SECT_MCPARA_M095_064	M095-064
SECT_MCPARA_M127_096	M127-096
SECT_MCPARA_M159_128	M159-128
SECT_MCPARA_M191_160	M191-160
SECT_MCPARA_M223_192	M223-192
SECT_MCPARA_M255_224	M255-224
SECT_MCPARA_AUX_AXS_NUM	AUX axis nos
SECT_MCPARA_AO0_IO_CH	AO[0] remote I/O Ch.
SECT_MCPARA_AO0_OFFSET	AO[0] offset
SECT_MCPARA_AO0_GAIN	AO[0] gain
SECT_MCPARA_AO1_IO_CH	AO[1] remote I/O Ch.
SECT_MCPARA_AO1_OFFSET	AO[1] offset
SECT_MCPARA_AO1_GAIN	AO[1] gain
SECT_MCPARA_AO2_IO_CH	AO[2] remote I/O Ch.
SECT_MCPARA_AO2_OFFSET	AO[2] offset
SECT_MCPARA_AO2_GAIN	AO[2] gain
SECT_MCPARA_AO3_IO_CH	AO[3] remote I/O Ch.
SECT_MCPARA_AO3_OFFSET	AO[3] offset
SECT_MCPARA_AO3_GAIN	AO[3] gain
SECT_MCPARA_AO4_IO_CH	AO[4] remote I/O Ch.
SECT_MCPARA_AO4_OFFSET	AO[4] offset
SECT_MCPARA_AO4_GAIN	AO[4] gain
SECT_MCPARA_AO5_IO_CH	AO[5] remote I/O Ch.
SECT_MCPARA_AO5_OFFSET	AO[5] offset
SECT_MCPARA_AO5_GAIN	AO[5] gain
SECT_MCPARA_AO6_IO_CH	AO[6] remote I/O Ch.
SECT_MCPARA_AO6_OFFSET	AO[6] offset

SECT_MCPARA_AO6_GAIN	AO[6] gain
SECT_MCPARA_AO7_IO_CH	AO[7] remote I/O Ch.
SECT_MCPARA_AO7_OFFSET	AO[7] offset
SECT_MCPARA_AO7_GAIN	AO[7] gain
SECT_MCPARA_AI0_IO_CH	AI[0] remote I/O Ch.
SECT_MCPARA_AI0_PORT_NO	AI[0] port No.
SECT_MCPARA_AI0_OFFSET	AI[0] offset
SECT_MCPARA_AI1_IO_CH	AI[1] remote I/O Ch.
SECT_MCPARA_AI1_PORT_NO	AI[1] port No.
SECT_MCPARA_AI1_OFFSET	AI[1] offset
SECT_MCPARA_AI2_IO_CH	AI[2] remote I/O Ch.
SECT_MCPARA_AI2_PORT_NO	AI[2] port No.
SECT_MCPARA_AI2_OFFSET	AI[2] offset
SECT_MCPARA_AI3_IO_CH	AI[3] remote I/O Ch.
SECT_MCPARA_AI3_PORT_NO	AI[3] port No.
SECT_MCPARA_AI3_OFFSET	AI[3] offset
SECT_MCPARA_RAPID	rapid
SECT_MCPARA_CLAMP	clamp
SECT_MCPARA_SMGST	smgst
SECT_MCPARA_G0TL	G0tL
SECT_MCPARA_G0T1	G0t1
SECT_MCPARA_G0T2	G0t2
SECT_MCPARA_G1TL	G1tL
SECT_MCPARA_G1T1	G1t1
SECT_MCPARA_G1T2	G1t2
SECT_MCPARA_OTTM	OTtm
SECT_MCPARA_G0BACK	G0back
SECT_MCPARA_G1BACK	G1back
SECT_MCPARA_OT_MINUS	OT -
SECT_MCPARA_OT_PLUS	OT +
SECT_MCPARA_TLML_MINUS	tlml -
SECT_MCPARA_TLML_PLUS	tlml +
SECT_MCPARA_PG0T	pG0t

SECT_MCPARA_PG1T	pG1t
SECT_MCPARA_TSKIP_T	tskip_T
SECT_MCPARA_PLRAP0	plrap0
SECT_MCPARA_PLRAP1	plrap1
SECT_MCPARA_PLCLMP	plclmp
SECT_MCPARA_OFFSET	offset
SECT_MCPARA_G0FWDG	G0fwdg
SECT_MCPARA_FWDG	fwd_g
SECT_MCPARA_VIR_AX	vir_ax
SECT_MCPARA_REF_MINUS	ref-
SECT_MCPARA_REF_PLUS	ref+
SECT_MCPARA_SPX_1	spx_1
SECT_MCPARA_SPX_2	_2
SECT_MCPARA_SPX_3	_3
SECT_MCPARA_BASEPS	baseps
SECT_MCPARA_M_CLAMP	m_clamp
SECT_MCPARA_PG0T3	pG0t3
SECT_MCPARA_PG1T3	pG1t3
SECT_MCPARA_PL3RAP0	pl3rap0
SECT_MCPARA_PL3RAP1	pl3rap1
SECT_MCPARA_PL3RAP2	pl3rap2
SECT_MCPARA_PL3CLMP0	pl3clmp0
SECT_MCPARA_PL3CLMP1	pl3clmp1
SECT_MCPARA_HANDLE	handle
SECT_MCPARA_TLML_1	tlml_1
SECT_MCPARA_THR_CLAMP	thr_clamp
SECT_MCPARA_THR_T1	thr_t1
SECT_MCPARA_ITF3_CLMP	itf3_clmp
SECT_MCPARA_G28RAP	G28rap
SECT_MCPARA_G28CRP	G28crp
SECT_MCPARA_G28SFT	G28sft
SECT_MCPARA_GRSPC	grspc
SECT_MCPARA_GRMASK	grmask

SECT_MCPARA_DIR_MINUS	dir(-)
SECT_MCPARA_NOREF	noref
SECT_MCPARA_Z_PULSE	Zp_no
SECT_MCPARA_NOCHK	nochk
SECT_MCPARA_1_RFP	#1_rfp
SECT_MCPARA_2_RFP	#2_rfp
SECT_MCPARA_3_RFP	#3_rfp
SECT_MCPARA_4_RFP	#4_rfp
SECT_MCPARA_CMPAX	cmpax
SECT_MCPARA_DRCAX	drcax
SECT_MCPARA_RDVNO	rdvno
SECT_MCPARA_MDVNO	mdvno
SECT_MCPARA_PDVNO	pdvno
SECT_MCPARA_SC	sc
SECT_MCPARA_SPCDV	spcdv
SECT_MCPARA_ERR_COMP(x)	Err. Compen Data
SECT_MCPARA_SV001	SV001
SECT_MCPARA_SV002	SV002
SECT_MCPARA_SV003	SV003
SECT_MCPARA_SV004	SV004
SECT_MCPARA_SV005	SV005
SECT_MCPARA_SV006	SV006
SECT_MCPARA_SV007	SV007
SECT_MCPARA_SV008	SV008
SECT_MCPARA_SV009	SV009
SECT_MCPARA_SV010	SV010
SECT_MCPARA_SV011	SV011
SECT_MCPARA_SV012	SV012
SECT_MCPARA_SV013	SV013
SECT_MCPARA_SV014	SV014
SECT_MCPARA_SV015	SV015
SECT_MCPARA_SV016	SV016
SECT_MCPARA_SV017	SV017

SECT_MCPARA_SV018	SV018
SECT_MCPARA_SV019	SV019
SECT_MCPARA_SV020	SV020
SECT_MCPARA_SV021	SV021
SECT_MCPARA_SV022	SV022
SECT_MCPARA_SV023	SV023
SECT_MCPARA_SV024	SV024
SECT_MCPARA_SV025	SV025
SECT_MCPARA_SV026	SV026
SECT_MCPARA_SV027	SV027
SECT_MCPARA_SV028	SV028
SECT_MCPARA_SV029	SV029
SECT_MCPARA_SV030	SV030
SECT_MCPARA_SV031	SV031
SECT_MCPARA_SV032	SV032
SECT_MCPARA_SV033	SV033
SECT_MCPARA_SV034	SV034
SECT_MCPARA_SV035	SV035
SECT_MCPARA_SV036	SV036
SECT_MCPARA_SV037	SV037
SECT_MCPARA_SV038	SV038
SECT_MCPARA_SV039	SV039
SECT_MCPARA_SV040	SV040
SECT_MCPARA_SV041	SV041
SECT_MCPARA_SV042	SV042
SECT_MCPARA_SV043	SV043
SECT_MCPARA_SV044	SV044
SECT_MCPARA_SV045	SV045
SECT_MCPARA_SV046	SV046
SECT_MCPARA_SV047	SV047
SECT_MCPARA_SV048	SV048
SECT_MCPARA_SV049	SV049
SECT_MCPARA_SV050	SV050

SECT_MCPARA_SV051	SV051
SECT_MCPARA_SV052	SV052
SECT_MCPARA_SV053	SV053
SECT_MCPARA_SV054	SV054
SECT_MCPARA_SV055	SV055
SECT_MCPARA_SV056	SV056
SECT_MCPARA_SV057	SV057
SECT_MCPARA_SV058	SV058
SECT_MCPARA_SV059	SV059
SECT_MCPARA_SV060	SV060
SECT_MCPARA_SV061	SV061
SECT_MCPARA_SV062	SV062
SECT_MCPARA_SV063	SV063
SECT_MCPARA_SV064	SV064
SECT_MCPATA_SV073	SV073
SECT_MCPARA_J2CT_MCPNO	mcp_no
SECT_MCPARA_J2CT_VIRAX	vir_ax
SECT_MCPARA_J2CT_MSR	MSR
SECT_MCPARA_J2CT_RTY	RTY
SECT_MCPARA_J2CT_PC1	PC1
SECT_MCPARA_J2CT_PC2	PC2
SECT_MCPARA_J2CT_PIT	PIT
SECT_MCPARA_J2CT_INP	INP
SECT_MCPARA_J2CT_ATU	ATU
SECT_MCPARA_J2CT_PG1	PG1
SECT_MCPARA_J2CT_EMGT	EMGt
SECT_MCPARA_J2CT_MBR	MBR
SECT_MCPARA_J2CT_NCH	NCH
SECT_MCPARA_J2CT_JIT	JIT
SECT_MCPARA_J2CT_PG2	PG2
SECT_MCPARA_J2CT_VG1	VG1
SECT_MCPARA_J2CT_VG2	VG2
SECT_MCPARA_J2CT_VIC	VIC

SECT_MCPARA_J2CT_VDC	VDC
SECT_MCPARA_J2CT_GD2	GD2
SECT_MCPARA_J2CT_MTY	MTY
SECT_MCPARA_J2CT_MD1	MD1
SECT_MCPARA_J2CT_MO1	MO1
SECT_MCPARA_J2CT_MD2	MD2
SECT_MCPARA_J2CT_MO2	MO2
SECT_MCPARA_J2CT_STATION	station
SECT_MCPARA_J2CT_CONT1	Cont1
SECT_MCPARA_J2CT_CONT2	Cont2
SECT_MCPARA_J2CT_EMGCONT	Emgcont
SECT_MCPARA_J2CT_TLENG	tleng
SECT_MCPARA_J2CT_ZRNSPEED	ZRNspeed
SECT_MCPARA_J2CT_ZRNCREEP	ZRNcreep
SECT_MCPARA_J2CT_GRIDMASK	grid mask
SECT_MCPARA_J2CT_GRSPC	grspc
SECT_MCPARA_J2CT_ZRNSHIFT	ZRNshift
SECT_MCPARA_J2CT_STOFFSET	ST.offset
SECT_MCPARA_J2CT_ABSBASE	ABS base
SECT_MCPARA_J2CT_LIMIT_PLUS	limit(+)
SECT_MCPARA_J2CT_LIMIT_MINUS	limit(-)
SECT_MCPARA_J2CT_ABSTYPE	ABS Type
SECT_MCPARA_J2CT_ABSCHECK	ABS check
SECT_MCPARA_J2CT_BACKLASH	backlash
SECT_MCPARA_J2CT_ASPEED1	Aspeed1
SECT_MCPARA_J2CT_MSPEED1	Mspeed1
SECT_MCPARA_J2CT_TIME1_1	time1.1
SECT_MCPARA_J2CT_TIME1_2	time1.2
SECT_MCPARA_J2CT_TL1	TL1
SECT_MCPARA_J2CT_OD1	OD1
SECT_MCPARA_J2CT_JUST1	just1
SECT_MCPARA_J2CT_NEAR1	near1
SECT_MCPARA_J2CT_ASPEED2	Aspeed2

SECT_MCPARA_J2CT_MSPEED2	Mspeed2
SECT_MCPARA_J2CT_TIME2_1	time2.1
SECT_MCPARA_J2CT_TIME2_2	time2.2
SECT_MCPARA_J2CT_TL2	TL2
SECT_MCPARA_J2CT_OD2	OD2
SECT_MCPARA_J2CT_JUST2	just2
SECT_MCPARA_J2CT_NEAR2	near2
SECT_MCPARA_J2CT_ASPEED3	Aspeed3
SECT_MCPARA_J2CT_MSPEED3	Mspeed3
SECT_MCPARA_J2CT_TIME3_1	time3.1
SECT_MCPARA_J2CT_TIME3_2	time3.2
SECT_MCPARA_J2CT_TL3	TL3
SECT_MCPARA_J2CT_OD3	OD3
SECT_MCPARA_J2CT_JUST3	just3
SECT_MCPARA_J2CT_NEAR3	near3
SECT_MCPARA_J2CT_ASPEED4	Aspeed4
SECT_MCPARA_J2CT_MSPEED4	Mspeed4
SECT_MCPARA_J2CT_TIME4_1	time4.1
SECT_MCPARA_J2CT_TIME4_2	time4.2
SECT_MCPARA_J2CT_TL4	TL4
SECT_MCPARA_J2CT_OD4	OD4
SECT_MCPARA_J2CT_JUST4	just4
SECT_MCPARA_J2CT_NEAR4	near4
SECT_MCPARA_J2CT_STPOS2	stpos2
SECT_MCPARA_J2CT_STPOS3	stpos3
SECT_MCPARA_J2CT_STPOS4	stpos4
SECT_MCPARA_J2CT_STPOS5	stpos5
SECT_MCPARA_J2CT_STPOS6	stpos6
SECT_MCPARA_J2CT_STPOS7	stpos7
SECT_MCPARA_J2CT_STPOS8	stpos8
SECT_MCPARA_J2CT_STPOS9	stpos9
SECT_MCPARA_J2CT_PSWCHECK	PSWcheck
SECT_MCPARA_J2CT_PSW1DOG1	PSW1dog1

SECT_MCPARA_J2CT_PSW1DOG2	PSW1dog2
SECT_MCPARA_J2CT_PSW2DOG1	PSW2dog1
SECT_MCPARA_J2CT_PSW2DOG2	PSW2dog2
SECT_MCPARA_J2CT_PSW3DOG1	PSW3dog1
SECT_MCPARA_J2CT_PSW3DOG2	PSW3dog2
SECT_MCPARA_J2CT_PSW4DOG1	PSW4dog1
SECT_MCPARA_J2CT_PSW4DOG2	PSW4dog2
SECT_MCPARA_J2CT_PSW5DOG1	PSW5dog1
SECT_MCPARA_J2CT_PSW5DOG2	PSW5dog2
SECT_MCPARA_J2CT_PSW6DOG1	PSW6dog1
SECT_MCPARA_J2CT_PSW6DOG2	PSW6dog2
SECT_MCPARA_J2CT_PSW7DOG1	PSW7dog1
SECT_MCPARA_J2CT_PSW7DOG2	PSW7dog2
SECT_MCPARA_J2CT_PSW8DOG1	PSW8dog1
SECT_MCPARA_J2CT_PSW8DOG2	PSW8dog2
SECT_MCPARA_J2CT_PUSH	push
SECT_MCPARA_J2CT_PUSHT1	pusht1
SECT_MCPARA_J2CT_PUSHT2	pusht2
SECT_MCPARA_J2CT_PUSHT3	pusht3
SECT_MCPARA_G01_CODE	G [01] <code>
SECT_MCPARA_G01_TYPE	<Type>
SECT_MCPARA_G01_PROGRAM_NO	<Program No.>
SECT_MCPARA_G02_CODE	G [02] <code>
SECT_MCPARA_G02_TYPE	<Type>
SECT_MCPARA_G02_PROGRAM_NO	<Program No.>
SECT_MCPARA_G03_CODE	G [03] <code>
SECT_MCPARA_G03_TYPE	<Type>
SECT_MCPARA_G03_PROGRAM_NO	<Program No.>
SECT_MCPARA_G04_CODE	G [04] <code>
SECT_MCPARA_G04_TYPE	<Type>
SECT_MCPARA_G04_PROGRAM_NO	<Program No.>
SECT_MCPARA_G05_CODE	G [05] <code>
SECT_MCPARA_G05_TYPE	<Type>

SECT_MCPARA_G05_PROGRAM_NO	<Program No.>
SECT_MCPARA_G06_CODE	G [06] <code>
SECT_MCPARA_G06_TYPE	<Type>
SECT_MCPARA_G06_PROGRAM_NO	<Program No.>
SECT_MCPARA_G07_CODE	G [07] <code>
SECT_MCPARA_G07_TYPE	<Type>
SECT_MCPARA_G07_PROGRAM_NO	<Program No.>
SECT_MCPARA_G08_CODE	G [08] <code>
SECT_MCPARA_G08_TYPE	<Type>
SECT_MCPARA_G08_PROGRAM_NO	<Program No.>
SECT_MCPARA_G09_CODE	G [09] <code>
SECT_MCPARA_G09_TYPE	<Type>
SECT_MCPARA_G09_PROGRAM_NO	<Program No.>
SECT_MCPARA_G10_CODE	G [10] <code>
SECT_MCPARA_G10_TYPE	<Type>
SECT_MCPARA_G10_PROGRAM_NO	<Program No.>
SECT_MCPARA_G200_CODE	G200 <code>
SECT_MCPARA_G200_PROGRAM_NO	<Program No.>
SECT_MCPARA_G300_CODE	G 300 <code>
SECT_MCPARA_G300_PROGRAM_NO	<Program No.>
SECT_MCPARA_G400_CODE	G400 <code>
SECT_MCPARA_G400_PROGRAM_NO	<Program No.>
SECT_MCPARA_G500_CODE	G 500 <code>
SECT_MCPARA_G500_PROGRAM_NO	<Program No.>
SECT_MCPARA_G600_CODE	G600 <code>
SECT_MCPARA_G600_PROGRAM_NO	<Program No.>
SECT_MCPARA_G700_CODE	G700 <code>
SECT_MCPARA_G700_PROGRAM_NO	<Program No.>
SECT_MCPARA_G800_CODE	G800 <code>
SECT_MCPARA_G800_PROGRAM_NO	<Program No.>
SECT_MCPARA_G900_CODE	G900 <code>
SECT_MCPARA_G900_PROGRAM_NO	<Program No.>
SECT_MCPARA_M01_CODE	M [01] <code>

SECT_MCPARA_M01_TYPE	<Type>
SECT_MCPARA_M01_PROGRAM_NO	<Program No.>
SECT_MCPARA_M02_CODE	M [02] <code>
SECT_MCPARA_M02_TYPE	<Type>
SECT_MCPARA_M02_PROGRAM_NO	<Program No.>
SECT_MCPARA_M03_CODE	M [03] <code>
SECT_MCPARA_M03_TYPE	<Type>
SECT_MCPARA_M03_PROGRAM_NO	<Program No.>
SECT_MCPARA_M04_CODE	M [04] <code>
SECT_MCPARA_M04_TYPE	<Type>
SECT_MCPARA_M04_PROGRAM_NO	<Program No.>
SECT_MCPARA_M05_CODE	M [05] <code>
SECT_MCPARA_M05_TYPE	<Type>
SECT_MCPARA_M05_PROGRAM_NO	<Program No.>
SECT_MCPARA_M06_CODE	M [06] <code>
SECT_MCPARA_M06_TYPE	<Type>
SECT_MCPARA_M06_PROGRAM_NO	<Program No.>
SECT_MCPARA_M07_CODE	M [07] <code>
SECT_MCPARA_M07_TYPE	<Type>
SECT_MCPARA_M07_PROGRAM_NO	<Program No.>
SECT_MCPARA_M08_CODE	M [08] <code>
SECT_MCPARA_M08_TYPE	<Type>
SECT_MCPARA_M08_PROGRAM_NO	<Program No.>
SECT_MCPARA_M09_CODE	M [09] <code>
SECT_MCPARA_M09_TYPE	<Type>
SECT_MCPARA_M09_PROGRAM_NO	<Program No.>
SECT_MCPARA_M10_CODE	M [10] <code>
SECT_MCPARA_M10_TYPE	<Type>
SECT_MCPARA_M10_PROGRAM_NO	<Program No.>
SECT_MCPARA_M11_CODE	M [11] <code>
SECT_MCPARA_M11_TYPE	<Type>
SECT_MCPARA_M11_PROGRAM_NO	<Program No.>
SECT_MCPARA_M12_CODE	M [12] <code>

SECT_MCPARA_M12_TYPE	<Type>
SECT_MCPARA_M12_PROGRAM_NO	<Program No.>
SECT_MCPARA_M13_CODE	M [13] <code>
SECT_MCPARA_M13_TYPE	<Type>
SECT_MCPARA_M13_PROGRAM_NO	<Program No.>
SECT_MCPARA_M14_CODE	M [14] <code>
SECT_MCPARA_M14_TYPE	<Type>
SECT_MCPARA_M14_PROGRAM_NO	<Program No.>
SECT_MCPARA_M15_CODE	M [15] <code>
SECT_MCPARA_M15_TYPE	<Type>
SECT_MCPARA_M15_PROGRAM_NO	<Program No.>
SECT_MCPARA_M16_CODE	M [16] <code>
SECT_MCPARA_M16_TYPE	<Type>
SECT_MCPARA_M16_PROGRAM_NO	<Program No.>
SECT_MCPARA_M17_CODE	M [17] <code>
SECT_MCPARA_M17_TYPE	<Type>
SECT_MCPARA_M17_PROGRAM_NO	<Program No.>
SECT_MCPARA_M18_CODE	M [18] <code>
SECT_MCPARA_M18_TYPE	<Type>
SECT_MCPARA_M18_PROGRAM_NO	<Program No.>
SECT_MCPARA_M19_CODE	M [19] <code>
SECT_MCPARA_M19_TYPE	<Type>
SECT_MCPARA_M19_PROGRAM_NO	<Program No.>
SECT_MCPARA_M20_CODE	M [20] <code>
SECT_MCPARA_M20_TYPE	<Type>
SECT_MCPARA_M20_PROGRAM_NO	<Program No.>
SECT_MCPARA_M21_CODE	M [21] <code>
SECT_MCPARA_M21_TYPE	<Type>
SECT_MCPARA_M21_PROGRAM_NO	<Program No.>
SECT_MCPARA_M22_CODE	M [22] <code>
SECT_MCPARA_M22_TYPE	<Type>
SECT_MCPARA_M22_PROGRAM_NO	<Program No.>
SECT_MCPARA_M23_CODE	M [23] <code>

SECT_MCPARA_M23_TYPE	<Type>
SECT_MCPARA_M23_PROGRAM_NO	<Program No.>
SECT_MCPARA_M24_CODE	M [24] <code>
SECT_MCPARA_M24_TYPE	<Type>
SECT_MCPARA_M24_PROGRAM_NO	<Program No.>
SECT_MCPARA_M25_CODE	M [25] <code>
SECT_MCPARA_M25_TYPE	<Type>
SECT_MCPARA_M25_PROGRAM_NO	<Program No.>
SECT_MCPARA_M26_CODE	M [26] <code>
SECT_MCPARA_M26_TYPE	<Type>
SECT_MCPARA_M26_PROGRAM_NO	<Program No.>
SECT_MCPARA_M27_CODE	M [27] <code>
SECT_MCPARA_M27_TYPE	<Type>
SECT_MCPARA_M27_PROGRAM_NO	<Program No.>
SECT_MCPARA_M28_CODE	M [28] <code>
SECT_MCPARA_M28_TYPE	<Type>
SECT_MCPARA_M28_PROGRAM_NO	<Program No.>
SECT_MCPARA_M29_CODE	M [29] <code>
SECT_MCPARA_M29_TYPE	<Type>
SECT_MCPARA_M29_PROGRAM_NO	<Program No.>
SECT_MCPARA_M30_CODE	M [30] <code>
SECT_MCPARA_M30_TYPE	<Type>
SECT_MCPARA_M30_PROGRAM_NO	<Program No.>
SECT_MCPARA_M31_CODE	M [31] <code>
SECT_MCPARA_M31_TYPE	<Type>
SECT_MCPARA_M31_PROGRAM_NO	<Program No.>
SECT_MCPARA_M32_CODE	M [32] <code>
SECT_MCPARA_M32_TYPE	<Type>
SECT_MCPARA_M32_PROGRAM_NO	<Program No.>
SECT_MCPARA_M33_CODE	M [33] <code>
SECT_MCPARA_M33_TYPE	<Type>
SECT_MCPARA_M33_PROGRAM_NO	<Program No.>
SECT_MCPARA_M34_CODE	M [34] <code>

SECT_MCPARA_M34_TYPE	<Type>
SECT_MCPARA_M34_PROGRAM_NO	<Program No.>
SECT_MCPARA_M35_CODE	M [35] <code>
SECT_MCPARA_M35_TYPE	<Type>
SECT_MCPARA_M35_PROGRAM_NO	<Program No.>
SECT_MCPARA_M36_CODE	M [36] <code>
SECT_MCPARA_M36_TYPE	<Type>
SECT_MCPARA_M36_PROGRAM_NO	<Program No.>
SECT_MCPARA_M37_CODE	M [37] <code>
SECT_MCPARA_M37_TYPE	<Type>
SECT_MCPARA_M37_PROGRAM_NO	<Program No.>
SECT_MCPARA_M38_CODE	M [38] <code>
SECT_MCPARA_M38_TYPE	<Type>
SECT_MCPARA_M38_PROGRAM_NO	<Program No.>
SECT_MCPARA_M39_CODE	M [39] <code>
SECT_MCPARA_M39_TYPE	<Type>
SECT_MCPARA_M39_PROGRAM_NO	<Program No.>
SECT_MCPARA_M40_CODE	M [40] <code>
SECT_MCPARA_M40_TYPE	<Type>
SECT_MCPARA_M40_PROGRAM_NO	<Program No.>
SECT_MCPARA_PCINT	PCint
SECT_MCPARA_M2MAC_TYPE	M2mac <type>
SECT_MCPARA_M2MAC_PROGRAM_NO	<Program No.>
SECT_MCPARA_SMAC_TYPE	Smac <type>
SECT_MCPARA_SMAC_PROGRAM_NO	<Program No.>
SECT_MCPARA_TMAC_TYPE	Tmac <type>
SECT_MCPARA_TMAC_PROGRAM_NO	<Program No.>
SECT_MCPARA_SLIMT1	slimt1
SECT_MCPARA_SLIMT2	_____2
SECT_MCPARA_SLIMT3	_____3
SECT_MCPARA_SLIMT4	_____4
SECT_MCPARA_SMAX1	smax 1
SECT_MCPARA_SMAX2	_____2

SECT_MCPARA_SMAX3	_____3
SECT_MCPARA_SMAX4	_____4
SECT_MCPARA_SSIFT1	ssift1
SECT_MCPARA_SSIFT2	_____2
SECT_MCPARA_SSIFT3	_____3
SECT_MCPARA_SSIFT4	_____4
SECT_MCPARA_STAP1	stap 1
SECT_MCPARA_STAP2	_____2
SECT_MCPARA_STAP3	_____3
SECT_MCPARA_STAP4	_____4
SECT_MCPARA_STAPT1	stapt1
SECT_MCPARA_STAPT2	_____2
SECT_MCPARA_STAPT3	_____3
SECT_MCPARA_STAPT4	_____4
SECT_MCPARA_SORI	sori
SECT_MCPARA_SGEAR	sgear
SECT_MCPARA_SMINI	smini
SECT_MCPARA_SERR	serr
SECT_MCPARA_SNAME	sname
SECT_MCPARA_SPRCMM	sprcmm
SECT_MCPARA_SENC_PNO	senc_pno
SECT_MCPARA_SANA_PNO	sana_pno
SECT_MCPARA_SPFLG	spflg
SECT_MCPARA_SENC_NO	senc_no
SECT_MCPARA_SANA_NO	sana_no
SECT_MCPARA_SMCP_NO	smcp_no
SECT_MCPARA_SPT	spt
SECT_MCPARA_SPRLV	sprlv
SECT_MCPARA_SPPLV	spplv
SECT_MCPARA_SPTC1	sptc1
SECT_MCPARA_SPTC2	sptc2
SECT_MCPARA_SPDIV1	spdiv1
SECT_MCPARA_SPDIV2	spdiv2

SECT_MCPARA_SPPLR	spplr
SECT_MCPARA_SPPST	sppst
SECT_MCPARA_GBSP	GBsp
SECT_MCPARA_SPTC3	sptc3
SECT_MCPARA_SPTC4	sptc4
SECT_MCPARA_SPTC5	sptc5
SECT_MCPARA_SPTC6	sptc6
SECT_MCPARA_SPTC7	sptc7
SECT_MCPARA_SPDIV3	spdiv3
SECT_MCPARA_SPDIV4	spdiv4
SECT_MCPARA_SPDIV5	spdiv5
SECT_MCPARA_SPDIV6	spdiv6
SECT_MCPARA_SPDIV7	spdiv7
SECT_MCPARA_SYNTM	syntm
SECT_MCPARA_SPYPRT	spyprt
SECT_MCPARA_SPS_1	sps_1
SECT_MCPARA_STAPBK	stapbk
SECT_MCPARA_SPHERR	spherr
SECT_MCPARA_SPHTC	sphtc
SECT_MCPARA_SFWD_G	sfwd_g
SECT_MCPARA_ORI_CS	ori_cs
SECT_MCPARA_SC_CS	sc_cs
SECT_MCPARA_TAP_SPD	tap_spd
SECT_MCPARA_TAP_SFT	tap_sft
SECT_MCPARA_TAP_T	tap_t
SECT_MCPARA_TAP_CS	tap_cs
SECT_MCPARA_SYC_CS	syc_cs
SECT_MCPARA_SGRA1	SGRA1
SECT_MCPARA_SGRA2	SGRA2
SECT_MCPARA_SGRA3	SGRA3
SECT_MCPARA_SGRA4	SGRA4
SECT_MCPARA_SGRB1	SGRB1
SECT_MCPARA_SGRB2	SGRB2

SECT_MCPARA_SGRB3	SGRB3
SECT_MCPARA_SGRB4	SGRB4
SECT_MCPARA_SLIMIT_L	slimit_L
SECT_MCPARA_ACS_TM	acs_tm
SECT_MCPARA_SP001	SP001
SECT_MCPARA_SP002	SP002
SECT_MCPARA_SP003	SP003
SECT_MCPARA_SP004	SP004
SECT_MCPARA_SP005	SP005
SECT_MCPARA_SP006	SP006
SECT_MCPARA_SP007	SP007
SECT_MCPARA_SP008	SP008
SECT_MCPARA_SP009	SP009
SECT_MCPARA_SP010	SP010
SECT_MCPARA_SP011	SP011
SECT_MCPARA_SP012	SP012
SECT_MCPARA_SP013	SP013
SECT_MCPARA_SP014	SP014
SECT_MCPARA_SP015	SP015
SECT_MCPARA_SP016	SP016
SECT_MCPARA_SP017	SP017
SECT_MCPARA_SP018	SP018
SECT_MCPARA_SP019	SP019
SECT_MCPARA_SP020	SP020
SECT_MCPARA_SP021	SP021
SECT_MCPARA_SP022	SP022
SECT_MCPARA_SP023	SP023
SECT_MCPARA_SP024	SP024
SECT_MCPARA_SP025	SP025
SECT_MCPARA_SP026	SP026
SECT_MCPARA_SP027	SP027
SECT_MCPARA_SP028	SP028
SECT_MCPARA_SP029	SP029

SECT_MCPARA_SP030	SP030
SECT_MCPARA_SP031	SP031
SECT_MCPARA_SP032	SP032
SECT_MCPARA_SP033	SP033
SECT_MCPARA_SP034	SP034
SECT_MCPARA_SP035	SP035
SECT_MCPARA_SP036	SP036
SECT_MCPARA_SP037	SP037
SECT_MCPARA_SP038	SP038
SECT_MCPARA_SP039	SP039
SECT_MCPARA_SP040	SP040
SECT_MCPARA_SP041	SP041
SECT_MCPARA_SP042	SP042
SECT_MCPARA_SP043	SP043
SECT_MCPARA_SP044	SP044
SECT_MCPARA_SP045	SP045
SECT_MCPARA_SP046	SP046
SECT_MCPARA_SP047	SP047
SECT_MCPARA_SP048	SP048
SECT_MCPARA_SP049	SP049
SECT_MCPARA_SP050	SP050
SECT_MCPARA_SP051	SP051
SECT_MCPARA_SP052	SP052
SECT_MCPARA_SP053	SP053
SECT_MCPARA_SP054	SP054
SECT_MCPARA_SP055	SP055
SECT_MCPARA_SP056	SP056
SECT_MCPARA_SP057	SP057
SECT_MCPARA_SP058	SP058
SECT_MCPARA_SP059	SP059
SECT_MCPARA_SP060	SP060
SECT_MCPARA_SP061	SP061
SECT_MCPARA_SP062	SP062

SECT_MCPARA_SP063	SP063
SECT_MCPARA_SP064	SP064
SECT_MCPARA_SP065	SP065
SECT_MCPARA_SP066	SP066
SECT_MCPARA_SP067	SP067
SECT_MCPARA_SP068	SP068
SECT_MCPARA_SP069	SP069
SECT_MCPARA_SP070	SP070
SECT_MCPARA_SP071	SP071
SECT_MCPARA_SP072	SP072
SECT_MCPARA_SP073	SP073
SECT_MCPARA_SP074	SP074
SECT_MCPARA_SP075	SP075
SECT_MCPARA_SP076	SP076
SECT_MCPARA_SP077	SP077
SECT_MCPARA_SP078	SP078
SECT_MCPARA_SP079	SP079
SECT_MCPARA_SP080	SP080
SECT_MCPARA_SP081	SP081
SECT_MCPARA_SP082	SP082
SECT_MCPARA_SP083	SP083
SECT_MCPARA_SP084	SP084
SECT_MCPARA_SP085	SP085
SECT_MCPARA_SP086	SP086
SECT_MCPARA_SP087	SP087
SECT_MCPARA_SP088	SP088
SECT_MCPARA_SP089	SP089
SECT_MCPARA_SP090	SP090
SECT_MCPARA_SP091	SP091
SECT_MCPARA_SP092	SP092
SECT_MCPARA_SP093	SP093
SECT_MCPARA_SP094	SP094
SECT_MCPARA_SP095	SP095

SECT_MCPARA_SP096	SP096
SECT_MCPARA_SP097	SP097
SECT_MCPARA_SP098	SP098
SECT_MCPARA_SP099	SP099
SECT_MCPARA_SP100	SP100
SECT_MCPARA_SP101	SP101
SECT_MCPARA_SP102	SP102
SECT_MCPARA_SP103	SP103
SECT_MCPARA_SP104	SP104
SECT_MCPARA_SP105	SP105
SECT_MCPARA_SP106	SP106
SECT_MCPARA_SP107	SP107
SECT_MCPARA_SP108	SP108
SECT_MCPARA_SP109	SP109
SECT_MCPARA_SP110	SP110
SECT_MCPARA_SP111	SP111
SECT_MCPARA_SP112	SP112
SECT_MCPARA_SP113	SP113
SECT_MCPARA_SP114	SP114
SECT_MCPARA_SP115	SP115
SECT_MCPARA_SP116	SP116
SECT_MCPARA_SP117	SP117
SECT_MCPARA_SP118	SP118
SECT_MCPARA_SP119	SP119
SECT_MCPARA_SP120	SP120
SECT_MCPARA_SP121	SP121
SECT_MCPARA_SP122	SP122
SECT_MCPARA_SP123	SP123
SECT_MCPARA_SP124	SP124
SECT_MCPARA_SP125	SP125
SECT_MCPARA_SP126	SP126
SECT_MCPARA_SP127	SP127
SECT_MCPARA_SP128	SP128

SECT_MCPARA_SP129	SP129
SECT_MCPARA_SP130	SP130
SECT_MCPARA_SP131	SP131
SECT_MCPARA_SP132	SP132
SECT_MCPARA_SP133	SP133
SECT_MCPARA_SP134	SP134
SECT_MCPARA_SP135	SP135
SECT_MCPARA_SP136	SP136
SECT_MCPARA_SP137	SP137
SECT_MCPARA_SP138	SP138
SECT_MCPARA_SP139	SP139
SECT_MCPARA_SP140	SP140
SECT_MCPARA_SP141	SP141
SECT_MCPARA_SP142	SP142
SECT_MCPARA_SP143	SP143
SECT_MCPARA_SP144	SP144
SECT_MCPARA_SP145	SP145
SECT_MCPARA_SP146	SP146
SECT_MCPARA_SP147	SP147
SECT_MCPARA_SP148	SP148
SECT_MCPARA_SP149	SP149
SECT_MCPARA_SP150	SP150
SECT_MCPARA_SP151	SP151
SECT_MCPARA_SP152	SP152
SECT_MCPARA_SP153	SP153
SECT_MCPARA_SP154	SP154
SECT_MCPARA_SP155	SP155
SECT_MCPARA_SP156	SP156
SECT_MCPARA_SP157	SP157
SECT_MCPARA_SP158	SP158
SECT_MCPARA_SP159	SP159
SECT_MCPARA_SP160	SP160
SECT_MCPARA_SP161	SP161

SECT_MCPARA_SP162	SP162
SECT_MCPARA_SP163	SP163
SECT_MCPARA_SP164	SP164
SECT_MCPARA_SP165	SP165
SECT_MCPARA_SP166	SP166
SECT_MCPARA_SP167	SP167
SECT_MCPARA_SP168	SP168
SECT_MCPARA_SP169	SP169
SECT_MCPARA_SP170	SP170
SECT_MCPARA_SP171	SP171
SECT_MCPARA_SP172	SP172
SECT_MCPARA_SP173	SP173
SECT_MCPARA_SP174	SP174
SECT_MCPARA_SP175	SP175
SECT_MCPARA_SP176	SP176
SECT_MCPARA_SP177	SP177
SECT_MCPARA_SP178	SP178
SECT_MCPARA_SP179	SP179
SECT_MCPARA_SP180	SP180
SECT_MCPARA_SP181	SP181
SECT_MCPARA_SP182	SP182
SECT_MCPARA_SP183	SP183
SECT_MCPARA_SP184	SP184
SECT_MCPARA_SP185	SP185
SECT_MCPARA_SP186	SP186
SECT_MCPARA_SP187	SP187
SECT_MCPARA_SP188	SP188
SECT_MCPARA_SP189	SP189
SECT_MCPARA_SP190	SP190
SECT_MCPARA_SP191	SP191
SECT_MCPARA_SP192	SP192
SECT_MCPARA_SP193	SP193
SECT_MCPARA_SP194	SP194

SECT_MCPARA_SP195	SP195
SECT_MCPARA_SP196	SP196
SECT_MCPARA_SP197	SP197
SECT_MCPARA_SP198	SP198
SECT_MCPARA_SP199	SP199
SECT_MCPARA_SP200	SP200
SECT_MCPARA_SP201	SP201
SECT_MCPARA_SP202	SP202
SECT_MCPARA_SP203	SP203
SECT_MCPARA_SP204	SP204
SECT_MCPARA_SP205	SP205
SECT_MCPARA_SP206	SP206
SECT_MCPARA_SP207	SP207
SECT_MCPARA_SP208	SP208
SECT_MCPARA_SP209	SP209
SECT_MCPARA_SP210	SP210
SECT_MCPARA_SP211	SP211
SECT_MCPARA_SP212	SP212
SECT_MCPARA_SP213	SP213
SECT_MCPARA_SP214	SP214
SECT_MCPARA_SP215	SP215
SECT_MCPARA_SP216	SP216
SECT_MCPARA_SP217	SP217
SECT_MCPARA_SP218	SP218
SECT_MCPARA_SP219	SP219
SECT_MCPARA_SP220	SP220
SECT_MCPARA_SP221	SP221
SECT_MCPARA_SP222	SP222
SECT_MCPARA_SP223	SP223
SECT_MCPARA_SP224	SP224
SECT_MCPARA_SP225	SP225
SECT_MCPARA_SP226	SP226
SECT_MCPARA_SP227	SP227

SECT_MCPARA_SP228	SP228
SECT_MCPARA_SP229	SP229
SECT_MCPARA_SP230	SP230
SECT_MCPARA_SP231	SP231
SECT_MCPARA_SP232	SP232
SECT_MCPARA_SP233	SP233
SECT_MCPARA_SP234	SP234
SECT_MCPARA_SP235	SP235
SECT_MCPARA_SP236	SP236
SECT_MCPARA_SP237	SP237
SECT_MCPARA_SP238	SP238
SECT_MCPARA_SP239	SP239
SECT_MCPARA_SP240	SP240
SECT_MCPARA_SP241	SP241
SECT_MCPARA_SP242	SP242
SECT_MCPARA_SP243	SP243
SECT_MCPARA_SP244	SP244
SECT_MCPARA_SP245	SP245
SECT_MCPARA_SP246	SP246
SECT_MCPARA_SP247	SP247
SECT_MCPARA_SP248	SP248
SECT_MCPARA_SP249	SP249
SECT_MCPARA_SP250	SP250
SECT_MCPARA_SP251	SP251
SECT_MCPARA_SP252	SP252
SECT_MCPARA_SP253	SP253
SECT_MCPARA_SP254	SP254
SECT_MCPARA_SP255	SP255
SECT_MCPARA_SP256	SP256
SECT_MCPARA_SP257	SP257
SECT_MCPARA_SP258	SP258
SECT_MCPARA_SP259	SP259
SECT_MCPARA_SP260	SP260

SECT_MCPARA_SP261	SP261
SECT_MCPARA_SP262	SP262
SECT_MCPARA_SP263	SP263
SECT_MCPARA_SP264	SP264
SECT_MCPARA_SP265	SP265
SECT_MCPARA_SP266	SP266
SECT_MCPARA_SP267	SP267
SECT_MCPARA_SP268	SP268
SECT_MCPARA_SP269	SP269
SECT_MCPARA_SP270	SP270
SECT_MCPARA_SP271	SP271
SECT_MCPARA_SP272	SP272
SECT_MCPARA_SP273	SP273
SECT_MCPARA_SP274	SP274
SECT_MCPARA_SP275	SP275
SECT_MCPARA_SP276	SP276
SECT_MCPARA_SP277	SP277
SECT_MCPARA_SP278	SP278
SECT_MCPARA_SP279	SP279
SECT_MCPARA_SP280	SP280
SECT_MCPARA_SP281	SP281
SECT_MCPARA_SP282	SP282
SECT_MCPARA_SP283	SP283
SECT_MCPARA_SP284	SP284
SECT_MCPARA_SP285	SP285
SECT_MCPARA_SP286	SP286
SECT_MCPARA_SP287	SP287
SECT_MCPARA_SP288	SP288
SECT_MCPARA_SP289	SP289
SECT_MCPARA_SP290	SP290
SECT_MCPARA_SP291	SP291
SECT_MCPARA_SP292	SP292
SECT_MCPARA_SP293	SP293

SECT_MCPARA_SP294	SP294
SECT_MCPARA_SP295	SP295
SECT_MCPARA_SP296	SP296
SECT_MCPARA_SP297	SP297
SECT_MCPARA_SP298	SP298
SECT_MCPARA_SP299	SP299
SECT_MCPARA_SP300	SP300
SECT_MCPARA_SP301	SP301
SECT_MCPARA_SP302	SP302
SECT_MCPARA_SP303	SP303
SECT_MCPARA_SP304	SP304
SECT_MCPARA_SP305	SP305
SECT_MCPARA_SP306	SP306
SECT_MCPARA_SP307	SP307
SECT_MCPARA_SP308	SP308
SECT_MCPARA_SP309	SP309
SECT_MCPARA_SP310	SP310
SECT_MCPARA_SP311	SP311
SECT_MCPARA_SP312	SP312
SECT_MCPARA_SP313	SP313
SECT_MCPARA_SP314	SP314
SECT_MCPARA_SP315	SP315
SECT_MCPARA_SP316	SP316
SECT_MCPARA_SP317	SP317
SECT_MCPARA_SP318	SP318
SECT_MCPARA_SP319	SP319
SECT_MCPARA_SP320	SP320
SECT_MCPARA_SP321	SP321
SECT_MCPARA_SP322	SP322
SECT_MCPARA_SP323	SP323
SECT_MCPARA_SP324	SP324
SECT_MCPARA_SP325	SP325
SECT_MCPARA_SP326	SP326

SECT_MCPARA_SP327	SP327
SECT_MCPARA_SP328	SP328
SECT_MCPARA_SP329	SP329
SECT_MCPARA_SP330	SP330
SECT_MCPARA_SP331	SP331
SECT_MCPARA_SP332	SP332
SECT_MCPARA_SP333	SP333
SECT_MCPARA_SP334	SP334
SECT_MCPARA_SP335	SP335
SECT_MCPARA_SP336	SP336
SECT_MCPARA_SP337	SP337
SECT_MCPARA_SP338	SP338
SECT_MCPARA_SP339	SP339
SECT_MCPARA_SP340	SP340
SECT_MCPARA_SP341	SP341
SECT_MCPARA_SP342	SP342
SECT_MCPARA_SP343	SP343
SECT_MCPARA_SP344	SP344
SECT_MCPARA_SP345	SP345
SECT_MCPARA_SP346	SP346
SECT_MCPARA_SP347	SP347
SECT_MCPARA_SP348	SP348
SECT_MCPARA_SP349	SP349
SECT_MCPARA_SP350	SP350
SECT_MCPARA_SP351	SP351
SECT_MCPARA_SP352	SP352
SECT_MCPARA_SP353	SP353
SECT_MCPARA_SP354	SP354
SECT_MCPARA_SP355	SP355
SECT_MCPARA_SP356	SP356
SECT_MCPARA_SP357	SP357
SECT_MCPARA_SP358	SP358
SECT_MCPARA_SP359	SP359

SECT_MCPARA_SP360	SP360
SECT_MCPARA_SP361	SP361
SECT_MCPARA_SP362	SP362
SECT_MCPARA_SP363	SP363
SECT_MCPARA_SP364	SP364
SECT_MCPARA_SP365	SP365
SECT_MCPARA_SP366	SP366
SECT_MCPARA_SP367	SP367
SECT_MCPARA_SP368	SP368
SECT_MCPARA_SP369	SP369
SECT_MCPARA_SP370	SP370
SECT_MCPARA_SP371	SP371
SECT_MCPARA_SP372	SP372
SECT_MCPARA_SP373	SP373
SECT_MCPARA_SP374	SP374
SECT_MCPARA_SP375	SP375
SECT_MCPARA_SP376	SP376
SECT_MCPARA_SP377	SP377
SECT_MCPARA_SP378	SP378
SECT_MCPARA_SP379	SP379
SECT_MCPARA_SP380	SP380
SECT_MCPARA_SP381	SP381
SECT_MCPARA_SP382	SP382
SECT_MCPARA_SP383	SP383
SECT_MCPARA_SP384	SP384
SECT_MCPARA_SPSV001	SPSV001
SECT_MCPARA_SPSV002	SPSV002
SECT_MCPARA_SPSV003	SPSV003
SECT_MCPARA_SPSV004	SPSV004
SECT_MCPARA_SPSV005	SPSV005
SECT_MCPARA_SPSV006	SPSV006
SECT_MCPARA_SPSV007	SPSV007
SECT_MCPARA_SPSV008	SPSV008

SECT_MCPARA_SPSV009	SPSV009
SECT_MCPARA_SPSV010	SPSV010
SECT_MCPARA_SPSV011	SPSV011
SECT_MCPARA_SPSV012	SPSV012
SECT_MCPARA_SPSV013	SPSV013
SECT_MCPARA_SPSV014	SPSV014
SECT_MCPARA_SPSV015	SPSV015
SECT_MCPARA_SPSV016	SPSV016
SECT_MCPARA_SPSV017	SPSV017
SECT_MCPARA_SPSV018	SPSV018
SECT_MCPARA_SPSV019	SPSV019
SECT_MCPARA_SPSV020	SPSV020
SECT_MCPARA_SPSV021	SPSV021
SECT_MCPARA_SPSV022	SPSV022
SECT_MCPARA_SPSV023	SPSV023
SECT_MCPARA_SPSV024	SPSV024
SECT_MCPARA_SPSV025	SPSV025
SECT_MCPARA_SPSV026	SPSV026
SECT_MCPARA_SPSV027	SPSV027
SECT_MCPARA_SPSV028	SPSV028
SECT_MCPARA_SPSV029	SPSV029
SECT_MCPARA_SPSV030	SPSV030
SECT_MCPARA_SPSV031	SPSV031
SECT_MCPARA_SPSV032	SPSV032
SECT_MCPARA_SPSV033	SPSV033
SECT_MCPARA_SPSV034	SPSV034
SECT_MCPARA_SPSV035	SPSV035
SECT_MCPARA_SPSV036	SPSV036
SECT_MCPARA_SPSV037	SPSV037
SECT_MCPARA_SPSV038	SPSV038
SECT_MCPARA_SPSV039	SPSV039
SECT_MCPARA_SPSV040	SPSV040
SECT_MCPARA_SPSV041	SPSV041

SECT_MCPARA_SPSV042	SPSV042
SECT_MCPARA_SPSV043	SPSV043
SECT_MCPARA_SPSV044	SPSV044
SECT_MCPARA_SPSV045	SPSV045
SECT_MCPARA_SPSV046	SPSV046
SECT_MCPARA_SPSV047	SPSV047
SECT_MCPARA_SPSV048	SPSV048
SECT_MCPARA_SPSV049	SPSV049
SECT_MCPARA_SPSV050	SPSV050
SECT_MCPARA_SPSV051	SPSV051
SECT_MCPARA_SPSV052	SPSV052
SECT_MCPARA_SPSV053	SPSV053
SECT_MCPARA_SPSV054	SPSV054
SECT_MCPARA_SPSV055	SPSV055
SECT_MCPARA_SPSV056	SPSV056
SECT_MCPARA_SPSV057	SPSV057
SECT_MCPARA_SPSV058	SPSV058
SECT_MCPARA_SPSV059	SPSV059
SECT_MCPARA_SPSV060	SPSV060
SECT_MCPARA_SPSV061	SPSV061
SECT_MCPARA_SPSV062	SPSV062
SECT_MCPARA_SPSV063	SPSV063
SECT_MCPARA_SPSV064	SPSV064
SECT_MCPARA_SPSV073	SPSV073
SECT_MCPARA_PLCVAL_01	- (Number only)
SECT_MCPARA_PLCVAL_02	- (Number only)
SECT_MCPARA_PLCVAL_03	- (Number only)
SECT_MCPARA_PLCVAL_04	- (Number only)
SECT_MCPARA_PLCVAL_05	- (Number only)
SECT_MCPARA_PLCVAL_06	- (Number only)
SECT_MCPARA_PLCVAL_07	- (Number only)
SECT_MCPARA_PLCVAL_08	- (Number only)
SECT_MCPARA_PLCVAL_09	- (Number only)

SECT_MCPARA_PLCVAL_10	- (Number only)
SECT_MCPARA_PLCVAL_11	- (Number only)
SECT_MCPARA_PLCVAL_12	- (Number only)
SECT_MCPARA_PLCVAL_13	- (Number only)
SECT_MCPARA_PLCVAL_14	- (Number only)
SECT_MCPARA_PLCVAL_15	- (Number only)
SECT_MCPARA_PLCVAL_16	- (Number only)
SECT_MCPARA_PLCVAL_17	- (Number only)
SECT_MCPARA_PLCVAL_18	- (Number only)
SECT_MCPARA_PLCVAL_19	- (Number only)
SECT_MCPARA_PLCVAL_20	- (Number only)
SECT_MCPARA_PLCVAL_21	- (Number only)
SECT_MCPARA_PLCVAL_22	- (Number only)
SECT_MCPARA_PLCVAL_23	- (Number only)
SECT_MCPARA_PLCVAL_24	- (Number only)
SECT_MCPARA_PLCVAL_25	- (Number only)
SECT_MCPARA_PLCVAL_26	- (Number only)
SECT_MCPARA_PLCVAL_27	- (Number only)
SECT_MCPARA_PLCVAL_28	- (Number only)
SECT_MCPARA_PLCVAL_29	- (Number only)
SECT_MCPARA_PLCVAL_30	- (Number only)
SECT_MCPARA_PLCVAL_31	- (Number only)
SECT_MCPARA_PLCVAL_32	- (Number only)
SECT_MCPARA_PLCVAL_33	- (Number only)
SECT_MCPARA_PLCVAL_34	- (Number only)
SECT_MCPARA_PLCVAL_35	- (Number only)
SECT_MCPARA_PLCVAL_36	- (Number only)
SECT_MCPARA_PLCVAL_37	- (Number only)
SECT_MCPARA_PLCVAL_38	- (Number only)
SECT_MCPARA_PLCVAL_39	- (Number only)
SECT_MCPARA_PLCVAL_40	- (Number only)
SECT_MCPARA_PLCVAL_41	- (Number only)
SECT_MCPARA_PLCVAL_42	- (Number only)

SECT_MCPARA_PLCVAL_43	- (Number only)
SECT_MCPARA_PLCVAL_44	- (Number only)
SECT_MCPARA_PLCVAL_45	- (Number only)
SECT_MCPARA_PLCVAL_46	- (Number only)
SECT_MCPARA_PLCVAL_47	- (Number only)
SECT_MCPARA_PLCVAL_48	- (Number only)
SECT_MCPARA_PLCTIMER_00	[10ms]
SECT_MCPARA_PLCTIMER_01	[10ms]
SECT_MCPARA_PLCTIMER_02	[10ms]
SECT_MCPARA_PLCTIMER_03	[10ms]
SECT_MCPARA_PLCTIMER_04	[10ms]
SECT_MCPARA_PLCTIMER_05	[10ms]
SECT_MCPARA_PLCTIMER_06	[10ms]
SECT_MCPARA_PLCTIMER_07	[10ms]
SECT_MCPARA_PLCTIMER_08	[10ms]
SECT_MCPARA_PLCTIMER_09	[10ms]
SECT_MCPARA_PLCTIMER_10	[10ms]
SECT_MCPARA_PLCTIMER_11	[10ms]
SECT_MCPARA_PLCTIMER_12	[10ms]
SECT_MCPARA_PLCTIMER_13	[10ms]
SECT_MCPARA_PLCTIMER_14	[10ms]
SECT_MCPARA_PLCTIMER_15	[10ms]
SECT_MCPARA_PLCTIMER_16	[100ms]
SECT_MCPARA_PLCTIMER_17	[100ms]
SECT_MCPARA_PLCTIMER_18	[100ms]
SECT_MCPARA_PLCTIMER_19	[100ms]
SECT_MCPARA_PLCTIMER_20	[100ms]
SECT_MCPARA_PLCTIMER_21	[100ms]
SECT_MCPARA_PLCTIMER_22	[100ms]
SECT_MCPARA_PLCTIMER_23	[100ms]
SECT_MCPARA_PLCTIMER_24	[100ms]
SECT_MCPARA_PLCTIMER_25	[100ms]
SECT_MCPARA_PLCTIMER_26	[100ms]

SECT_MCPARA_PLCTIMER_27	[100ms]
SECT_MCPARA_PLCTIMER_28	[100ms]
SECT_MCPARA_PLCTIMER_29	[100ms]
SECT_MCPARA_PLCTIMER_30	[100ms]
SECT_MCPARA_PLCTIMER_31	[100ms]
SECT_MCPARA_PLCTIMER_32	[100ms]
SECT_MCPARA_PLCTIMER_33	[100ms]
SECT_MCPARA_PLCTIMER_34	[100ms]
SECT_MCPARA_PLCTIMER_35	[100ms]
SECT_MCPARA_PLCTIMER_36	[100ms]
SECT_MCPARA_PLCTIMER_37	[100ms]
SECT_MCPARA_PLCTIMER_38	[100ms]
SECT_MCPARA_PLCTIMER_39	[100ms]
SECT_MCPARA_PLCTIMER_40	[100ms]
SECT_MCPARA_PLCTIMER_41	[100ms]
SECT_MCPARA_PLCTIMER_42	[100ms]
SECT_MCPARA_PLCTIMER_43	[100ms]
SECT_MCPARA_PLCTIMER_44	[100ms]
SECT_MCPARA_PLCTIMER_45	[100ms]
SECT_MCPARA_PLCTIMER_46	[100ms]
SECT_MCPARA_PLCTIMER_47	[100ms]
SECT_MCPARA_PLCTIMER_48	[100ms]
SECT_MCPARA_PLCTIMER_49	[100ms]
SECT_MCPARA_PLCTIMER_50	[100ms]
SECT_MCPARA_PLCTIMER_51	[100ms]
SECT_MCPARA_PLCTIMER_52	[100ms]
SECT_MCPARA_PLCTIMER_53	[100ms]
SECT_MCPARA_PLCTIMER_54	[100ms]
SECT_MCPARA_PLCTIMER_55	[100ms]
SECT_MCPARA_PLCTIMER_56	[100ms]
SECT_MCPARA_PLCTIMER_57	[100ms]
SECT_MCPARA_PLCTIMER_58	[100ms]
SECT_MCPARA_PLCTIMER_59	[100ms]

SECT_MCPARA_PLCTIMER_60	[100ms]
SECT_MCPARA_PLCTIMER_61	[100ms]
SECT_MCPARA_PLCTIMER_62	[100ms]
SECT_MCPARA_PLCTIMER_63	[100ms]
SECT_MCPARA_PLCTIMER_64	[100ms]
SECT_MCPARA_PLCTIMER_65	[100ms]
SECT_MCPARA_PLCTIMER_66	[100ms]
SECT_MCPARA_PLCTIMER_67	[100ms]
SECT_MCPARA_PLCTIMER_68	[100ms]
SECT_MCPARA_PLCTIMER_69	[100ms]
SECT_MCPARA_PLCTIMER_70	[100ms]
SECT_MCPARA_PLCTIMER_71	[100ms]
SECT_MCPARA_PLCTIMER_72	[100ms]
SECT_MCPARA_PLCTIMER_73	[100ms]
SECT_MCPARA_PLCTIMER_74	[100ms]
SECT_MCPARA_PLCTIMER_75	[100ms]
SECT_MCPARA_PLCTIMER_76	[100ms]
SECT_MCPARA_PLCTIMER_77	[100ms]
SECT_MCPARA_PLCTIMER_78	[100ms]
SECT_MCPARA_PLCTIMER_79	[100ms]
SECT_MCPARA_PLCTIMER_80	[100ms]
SECT_MCPARA_PLCTIMER_81	[100ms]
SECT_MCPARA_PLCTIMER_82	[100ms]
SECT_MCPARA_PLCTIMER_83	[100ms]
SECT_MCPARA_PLCTIMER_84	[100ms]
SECT_MCPARA_PLCTIMER_85	[100ms]
SECT_MCPARA_PLCTIMER_86	[100ms]
SECT_MCPARA_PLCTIMER_87	[100ms]
SECT_MCPARA_PLCTIMER_88	[100ms]
SECT_MCPARA_PLCTIMER_89	[100ms]
SECT_MCPARA_PLCTIMER_90	[100ms]
SECT_MCPARA_PLCTIMER_91	[100ms]
SECT_MCPARA_PLCTIMER_92	[100ms]

SECT_MCPARA_PLCTIMER_93	[100ms]
SECT_MCPARA_PLCTIMER_94	[100ms]
SECT_MCPARA_PLCTIMER_95	[100ms]
SECT_MCPARA_PLCTIMER_96	[100ms INC.]
SECT_MCPARA_PLCTIMER_97	[100ms INC.]
SECT_MCPARA_PLCTIMER_98	[100ms INC.]
SECT_MCPARA_PLCTIMER_99	[100ms INC.]
SECT_MCPARA_PLCTIMER_100	[100ms INC.]
SECT_MCPARA_PLCTIMER_101	[100ms INC.]
SECT_MCPARA_PLCTIMER_102	[100ms INC.]
SECT_MCPARA_PLCTIMER_103	[100ms INC.]
SECT_MCPARA_PLCCOUNTER_00	- (Number only)
SECT_MCPARA_PLCCOUNTER_01	- (Number only)
SECT_MCPARA_PLCCOUNTER_02	- (Number only)
SECT_MCPARA_PLCCOUNTER_03	- (Number only)
SECT_MCPARA_PLCCOUNTER_04	- (Number only)
SECT_MCPARA_PLCCOUNTER_05	- (Number only)
SECT_MCPARA_PLCCOUNTER_06	- (Number only)
SECT_MCPARA_PLCCOUNTER_07	- (Number only)
SECT_MCPARA_PLCCOUNTER_08	- (Number only)
SECT_MCPARA_PLCCOUNTER_09	- (Number only)
SECT_MCPARA_PLCCOUNTER_10	- (Number only)
SECT_MCPARA_PLCCOUNTER_11	- (Number only)
SECT_MCPARA_PLCCOUNTER_12	- (Number only)
SECT_MCPARA_PLCCOUNTER_13	- (Number only)
SECT_MCPARA_PLCCOUNTER_14	- (Number only)
SECT_MCPARA_PLCCOUNTER_15	- (Number only)
SECT_MCPARA_PLCCOUNTER_16	- (Number only)
SECT_MCPARA_PLCCOUNTER_17	- (Number only)
SECT_MCPARA_PLCCOUNTER_18	- (Number only)
SECT_MCPARA_PLCCOUNTER_19	- (Number only)
SECT_MCPARA_PLCCOUNTER_20	- (Number only)
SECT_MCPARA_PLCCOUNTER_21	- (Number only)

SECT_MCPARA_PLCCOUNTER_22	- (Number only)
SECT_MCPARA_PLCCOUNTER_23	- (Number only)
SECT_MCPARA_BITSEL_01	- (Number only)
SECT_MCPARA_BITSEL_02	- (Number only)
SECT_MCPARA_BITSEL_03	- (Number only)
SECT_MCPARA_BITSEL_04	- (Number only)
SECT_MCPARA_BITSEL_05	- (Number only)
SECT_MCPARA_BITSEL_06	- (Number only)
SECT_MCPARA_BITSEL_07	- (Number only)
SECT_MCPARA_BITSEL_08	- (Number only)
SECT_MCPARA_BITSEL_09	- (Number only)
SECT_MCPARA_BITSEL_10	- (Number only)
SECT_MCPARA_BITSEL_11	- (Number only)
SECT_MCPARA_BITSEL_12	- (Number only)
SECT_MCPARA_BITSEL_13	- (Number only)
SECT_MCPARA_BITSEL_14	- (Number only)
SECT_MCPARA_BITSEL_15	- (Number only)
SECT_MCPARA_BITSEL_16	- (Number only)
SECT_MCPARA_BITSEL_17	- (Number only)
SECT_MCPARA_BITSEL_18	- (Number only)
SECT_MCPARA_BITSEL_19	- (Number only)
SECT_MCPARA_BITSEL_20	- (Number only)
SECT_MCPARA_BITSEL_21	- (Number only)
SECT_MCPARA_BITSEL_22	- (Number only)
SECT_MCPARA_BITSEL_23	- (Number only)
SECT_MCPARA_BITSEL_24	- (Number only)
SECT_MCPARA_BITSEL_25	- (Number only)
SECT_MCPARA_BITSEL_26	- (Number only)
SECT_MCPARA_BITSEL_27	- (Number only)
SECT_MCPARA_BITSEL_28	- (Number only)
SECT_MCPARA_BITSEL_29	- (Number only)
SECT_MCPARA_BITSEL_30	- (Number only)
SECT_MCPARA_BITSEL_31	- (Number only)

SECT_MCPARA_BITSEL_32	- (Number only)
SECT_MCPARA_BITSEL_33	- (Number only)
SECT_MCPARA_BITSEL_34	- (Number only)
SECT_MCPARA_BITSEL_35	- (Number only)
SECT_MCPARA_BITSEL_36	- (Number only)
SECT_MCPARA_BITSEL_37	- (Number only)
SECT_MCPARA_BITSEL_38	- (Number only)
SECT_MCPARA_BITSEL_39	- (Number only)
SECT_MCPARA_BITSEL_40	- (Number only)
SECT_MCPARA_BITSEL_41	- (Number only)
SECT_MCPARA_BITSEL_42	- (Number only)
SECT_MCPARA_BITSEL_43	- (Number only)
SECT_MCPARA_BITSEL_44	- (Number only)
SECT_MCPARA_BITSEL_45	- (Number only)
SECT_MCPARA_BITSEL_46	- (Number only)
SECT_MCPARA_BITSEL_47	- (Number only)
SECT_MCPARA_BITSEL_48	- (Number only)
SECT_MCPARA_BITSEL_49	- (Number only)
SECT_MCPARA_BITSEL_50	- (Number only)
SECT_MCPARA_BITSEL_51	- (Number only)
SECT_MCPARA_BITSEL_52	- (Number only)
SECT_MCPARA_BITSEL_53	- (Number only)
SECT_MCPARA_BITSEL_54	- (Number only)
SECT_MCPARA_BITSEL_55	- (Number only)
SECT_MCPARA_BITSEL_56	- (Number only)
SECT_MCPARA_BITSEL_57	- (Number only)
SECT_MCPARA_BITSEL_58	- (Number only)
SECT_MCPARA_BITSEL_59	- (Number only)
SECT_MCPARA_BITSEL_60	- (Number only)
SECT_MCPARA_BITSEL_61	- (Number only)
SECT_MCPARA_BITSEL_62	- (Number only)
SECT_MCPARA_BITSEL_63	- (Number only)
SECT_MCPARA_BITSEL_64	- (Number only)

SECT_MCPARA_BITSEL_65	- (Number only)
SECT_MCPARA_BITSEL_66	- (Number only)
SECT_MCPARA_BITSEL_67	- (Number only)
SECT_MCPARA_BITSEL_68	- (Number only)
SECT_MCPARA_BITSEL_69	- (Number only)
SECT_MCPARA_BITSEL_70	- (Number only)
SECT_MCPARA_BITSEL_71	- (Number only)
SECT_MCPARA_BITSEL_72	- (Number only)
SECT_MCPARA_BITSEL_73	- (Number only)
SECT_MCPARA_BITSEL_74	- (Number only)
SECT_MCPARA_BITSEL_75	- (Number only)
SECT_MCPARA_BITSEL_76	- (Number only)
SECT_MCPARA_BITSEL_77	- (Number only)
SECT_MCPARA_BITSEL_78	- (Number only)
SECT_MCPARA_BITSEL_79	- (Number only)
SECT_MCPARA_BITSEL_80	- (Number only)
SECT_MCPARA_BITSEL_81	- (Number only)
SECT_MCPARA_BITSEL_82	- (Number only)
SECT_MCPARA_BITSEL_83	- (Number only)
SECT_MCPARA_BITSEL_84	- (Number only)
SECT_MCPARA_BITSEL_85	- (Number only)
SECT_MCPARA_BITSEL_86	- (Number only)
SECT_MCPARA_BITSEL_87	- (Number only)
SECT_MCPARA_BITSEL_88	- (Number only)
SECT_MCPARA_BITSEL_89	- (Number only)
SECT_MCPARA_BITSEL_90	- (Number only)
SECT_MCPARA_BITSEL_91	- (Number only)
SECT_MCPARA_BITSEL_92	- (Number only)
SECT_MCPARA_BITSEL_93	- (Number only)
SECT_MCPARA_BITSEL_94	- (Number only)
SECT_MCPARA_BITSEL_95	- (Number only)
SECT_MCPARA_BITSEL_96	- (Number only)
SECT_SYPARA_M_OP01	m_op01

SECT_SYPARA_M_OP02	m_op02
SECT_SYPARA_M_OP03	m_op03
SECT_SYPARA_M_OP04	m_op04
SECT_SYPARA_M_OP05	m_op05
SECT_SYPARA_M_OP06	m_op06
SECT_SYPARA_M_OP07	m_op07
SECT_SYPARA_M_OP08	m_op08
SECT_SYPARA_M_OP09	m_op09
SECT_SYPARA_M_OP10	m_op10
SECT_SYPARA_M_OP11	m_op11
SECT_SYPARA_M_OP12	m_op12
SECT_SYPARA_M_OP13	m_op13
SECT_SYPARA_M_OP14	m_op14
SECT_SYPARA_M_OP15	m_op15
SECT_SYPARA_M_OP16	m_op16
SECT_SYPARA_M_OP17	m_op17
SECT_SYPARA_M_OP18	m_op18
SECT_SYPARA_M_OP19	m_op19
SECT_SYPARA_M_OP20	m_op20
SECT_SYPARA_M_OP21	m_op21
SECT_SYPARA_M_OP22	m_op22
SECT_SYPARA_M_OP23	m_op23
SECT_SYPARA_M_OP24	m_op24
SECT_SYPARA_M_OP25	m_op25
SECT_SYPARA_M_OP26	m_op26
SECT_SYPARA_M_OP27	m_op27
SECT_SYPARA_M_OP28	m_op28
SECT_SYPARA_M_OP29	m_op29
SECT_SYPARA_M_OP30	m_op30
SECT_SYPARA_M_OP31	m_op31
SECT_SYPARA_M_OP32	m_op32
SECT_SYPARA_M_OP33	m_op33
SECT_SYPARA_M_OP34	m_op34

SECT_SYPARA_M_OP35	m_op35
SECT_SYPARA_M_OP36	m_op36
SECT_SYPARA_M_OP37	m_op37
SECT_SYPARA_M_OP38	m_op38
SECT_SYPARA_M_OP39	m_op39
SECT_SYPARA_M_OP40	m_op40
SECT_SYPARA_IUNIT_S	iunit_s
SECT_SYPARA_OFMEMO	ofmemo
SECT_SYPARA_OFNUMB	ofnumb
SECT_SYPARA_VALTYP	valtyp
SECT_SYPARA_TLFSIZ	tlfsiz
SECT_SYPARA_PROSIZ	prosiz
SECT_SYPARA_MAC_SIZE	mac_size
SECT_SYPARA_PLCAX	plcax
SECT_SYPARA_SPAX	spax
SECT_SYPARA_CSTMVTYP	cstmvtyp
SECT_SYPARA_CSFILESZ	custom file size
SECT_SYPARA_SYSON_SYS1	syson $1~10
SECT_SYPARA_SYSON_SYS2	syson $1~10
SECT_SYPARA_SYSON_SYS3	syson $1~10
SECT_SYPARA_SYSON_SYS4	syson $1~10
SECT_SYPARA_SYSON_SYS5	syson $1~10
SECT_SYPARA_SYSON_SYS6	syson $1~10
SECT_SYPARA_SYSON_SYS7	syson $1~10
SECT_SYPARA_SYSON_SYS8	syson $1~10
SECT_SYPARA_SYSON_SYS9	syson $1~10
SECT_SYPARA_SYSON_SYS10	syson $1~10
SECT_SYPARA_AXISNO_SYS1	AXISNO $1~10
SECT_SYPARA_AXISNO_SYS2	AXISNO $1~10
SECT_SYPARA_AXISNO_SYS3	AXISNO $1~10
SECT_SYPARA_AXISNO_SYS4	AXISNO $1~10
SECT_SYPARA_AXISNO_SYS5	AXISNO $1~10
SECT_SYPARA_AXISNO_SYS6	AXISNO $1~10

SECT_SYPARA_AXISNO_SYS7	AXISNO $1~10
SECT_SYPARA_AXISNO_SYS8	AXISNO $1~10
SECT_SYPARA_AXISNO_SYS9	AXISNO $1~10
SECT_SYPARA_AXISNO_SYS10	AXISNO $1~10
SECT_SYPARA_SIMLAX_SYS1	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS2	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS3	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS4	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS5	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS6	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS7	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS8	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS9	SIMLAX $1~10
SECT_SYPARA_SIMLAX_SYS10	SIMLAX $1~10
SECT_SYPARA_ISYNCT_SYS1	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS2	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS3	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS4	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS5	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS6	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS7	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS8	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS9	ISYNCT $1~10
SECT_SYPARA_ISYNCT_SYS10	ISYNCT $1~10
SECT_SYPARA_MOTION_SYS1	MOTION $1~10
SECT_SYPARA_MOTION_SYS2	MOTION $1~10
SECT_SYPARA_MOTION_SYS3	MOTION $1~10
SECT_SYPARA_MOTION_SYS4	MOTION $1~10
SECT_SYPARA_MOTION_SYS5	MOTION $1~10
SECT_SYPARA_MOTION_SYS6	MOTION $1~10
SECT_SYPARA_MOTION_SYS7	MOTION $1~10
SECT_SYPARA_MOTION_SYS8	MOTION $1~10
SECT_SYPARA_MOTION_SYS9	MOTION $1~10

SECT_SYPARA_MOTION_SYS10	MOTION $1~10
SECT_SYPARA_PC_HT	pc_ht
SECT_SYPARA_PC_HW	pc_hw
SECT_SYPARA_PC_MT	pc_mt
SECT_SYPARA_PC_MW	pc_mw
SECT_SYPARA_PC_MTY	pc_mty
SECT_SYPARA_PC_LT	pc_lt
SECT_SYPARA_PC_B1T	pc_b1t
SECT_SYPARA_PC_B2T	pc_b2t
SECT_SYPARA_CSFILESZ	custom file size
SECT_SYPARA_DS_STDSZ	data_srv std size
SECT_SYPARA_DS_MAXSZ	data_srv max size
SECT_USPARA_DRY_RUN	G00 dry run
SECT_USPARA_MACRO_SINGLE	Macro single
SECT_USPARA_MPOINT_NEGLECT	Ignore middle point
SECT_USPARA_MCLOCK_RAPID	Machine lock high speed
SECT_USPARA_ABS_INC_ADDR	ABS / INC address
SECT_USPARA_G04_FIXED_TIME	G04 Time fixed
SECT_USPARA_COLL_ALM_OFF	Interference avoidance
SECT_USPARA_UNIT_10	Command unit 10 times
SECT_USPARA_DECIMAL_PNT_2	Decimal type 2
SECT_USPARA_G0_INTERP_OFF	G00 non-interpolation
SECT_USPARA_THREAD_LEAD_E	Precision Threading E
SECT_USPARA_NOSER_CMP_B	Cutting edge correction type B
SECT_USPARA_EXT_DEC_OFF	Disable external deceleration
SECT_USPARA_INITIAL_INCH	Initial inch
SECT_USPARA_INITIAL_ABS	Initial absolute value
SECT_USPARA_INITIAL_SYNC	Initial synchronous feed
SECT_USPARA_INITIAL_G96	Initial circumferential speed constant
SECT_USPARA_INITIAL_ZX	Initial Z - X plane
SECT_USPARA_INITIAL_YZ	Initial Y - Z plane
SECT_USPARA_G00	Initial G00
SECT_USPARA_G83_G87_RAPID	G83 / 87 high speed

SECT_USPARA_FIXED_C_MODAL	Fixed cycle modal
SECT_USPARA_LAP_CYCLE_MODE	Turning cycle mode
SECT_USPARA_SYNC_TAP	Sync tap
SECT_USPARA_TLIFE_CHECK	Lifetime management effective
SECT_USPARA_GCODE_TYPE_1	G code type (1)
SECT_USPARA_GCODE_TYPE_2	G code type (2)
SECT_USPARA_GCODE_TYPE_3	G code type (3)
SECT_USPARA_MANUAL_IT_RST	Reset interrupt level
SECT_USPARA_G46_NO_REV_ERR	G46 Inversion error avoidance
SECT_USPARA_SHORTCUT_RT	Closer axis of rotation
SECT_USPARA_EDIT_LOCK_B	Edit lock B
SECT_USPARA_START_POINT_ALM	Start point alarm
SECT_USPARA_MILLING_G16	Milling G 16
SECT_USPARA_MILLING_G19	Milling G 19
SECT_USPARA_HOST_LINK	Upper communication
SECT_USPARA_SP_ERR_Z2_Z1	Superimposition correction Z2 / Z1
SECT_USPARA_SP_ERR_Z3_Z1	Superimposition correction Z3 / Z1
SECT_USPARA_SP_ERR_Z2_Z3	Superimposition correction Z3 / Z2
SECT_USPARA_INITIAL_BARRIER	Initial barrier
SECT_USPARA_BARRIER_OFF	Barrier disabled
SECT_USPARA_TOOL_SET_TYPE_2	Correction setting method 2
SECT_USPARA_SCREEN_COPY	Screen hard copy
SECT_USPARA_MIRR_IMAGE	Mirror image
SECT_USPARA_DOG_TYPE_A	Automatic dog type
SECT_USPARA_DOG_TYPE_M	Manual dog type
SECT_USPARA_AX_RELEASE	Shaft removal
SECT_USPARA_OT_CHECK_OFF	Soft limit invalid
SECT_USPARA_OT_CHECK_N	Soft limit (-)
SECT_USPARA_OT_CHECK_P	Soft limit (+)
SECT_USPARA_POINT_OF_TOOL_CHANGE	Tool change position
SECT_USPARA_PLANE_I	Plane <I>
SECT_USPARA_PLANE_J	<J>
SECT_USPARA_PLANE_K	<K>

SECT_USPARA_AUX_PLANE_I	Parallel axis <I>
SECT_USPARA_AUX_PLANE_J	<J>
SECT_USPARA_AUX_PLANE_K	<K>
SECT_USPARA_G02_G03_ERROR	Arc error
SECT_USPARA_CHAMFER_VALUE	Amount of chamfer
SECT_USPARA_CHAMFER_ANGLE	angle
SECT_USPARA_G71_MIN_THICK	G71 minimum incision
SECT_USPARA_G71_DELTA_D	Cutting change
SECT_USPARA_G71_PULL_UP	escape
SECT_USPARA_G71_THICK	Incision
SECT_USPARA_G74_RETRACT	G74 return
SECT_USPARA_G76_FINISHING	G76 finishing allowance
SECT_USPARA_G76_MIN_THICK	Minimum incision
SECT_USPARA_G76_TIMES	Times
SECT_USPARA_G76_ANGLE	Thread
SECT_USPARA_G71_POCKET	G71 pocket processing
SECT_USPARA_G73_CUT_X	G73 shaving X
SECT_USPARA_G73_CUT_Z	Shaving Z
SECT_USPARA_G73_TIMES	Times
SECT_USPARA_G83_RETRACT	G83 return
SECT_USPARA_TIP_OFFSET_MAX	Wear maximum value
SECT_USPARA_TIP_OFFSET_INC_MAX	Maximum addition value
SECT_USPARA_AUTO_TLM_SPEED	Tool measurement speed
SECT_USPARA_AUTO_TLM_ZONE_R	Region r
SECT_USPARA_AUTO_TLM_ZONE_D	Region r
SECT_USPARA_SYNC_SYSTEM	Waiting system
SECT_USPARA_CONST_SPEED	Rate constant
SECT_USPARA_INTERVAL	Feed time
SECT_USPARA_CONTROL	Control parameters
SECT_USPARA_SCR_TIME_OUT_SET	[Screensaver] Timeout time
SECT_USPARA_NRMS_TIME_OUT_SET	[Interference avoidance] Timeout time
SECT_USPARA_ANGLE	Corner deceleration check angle
SECT_USPARA_WIDTH	Corner deceleration check width

SECT_USPARA_ANGLE_G0_TO_G1	G1 ? G0 deceleration check width
SECT_USPARA_COUNTER_SELECT_TYPE	Counter selection invalid *
SECT_USPARA_TEST_00	Test mode
SECT_USPARA_DISP_MENU	Select display menu
SECT_USPARA_PROGRAM_SAVE_TYPE	Program saving method
SECT_USPARA_STN_CONTRAST	STN contrast
SECT_USPARA_EDITOR_INSERT_SP	Edit Space Insertion Display
SECT_USPARA_FREEZE_MENU_INVALID	Gray menu disabled *
SECT_USPARA_ABS_INC_STATUS	Absolute addition state
SECT_USPARA_KEYBOARD_TYPE_SELECT	PC keyboard type selection *
SECT_USPARA_MAINPRO_STORAGE	Main program storage location
SECT_USPARA_GRAPHIC_DISP_SCALE	Graphic Display Scale
SECT_USPARA_GRAPHIC_DISP_MODE	Graphic display mode
SECT_USPARA_GRAPHIC_COORD_CHG	Graphic coordinate switching
SECT_USPARA_GRAPHIC_DIRECT_I	Graphic drawing magnification I
SECT_USPARA_GRAPHIC_DIRECT_J	Graphic drawing magnification J
SECT_USPARA_GRAPHIC_DIRECT_K	Graphic drawing magnification K
SECT_USPARA_BARRIER_X1	X (chuck barrier P1 x coordinate)
SECT_USPARA_BARRIER_Z1	Z (chuck barrier P1 Z coordinate)
SECT_USPARA_BARRIER_X2	X (chuck barrier P2 X coordinate)
SECT_USPARA_BARRIER_Z2	Z (chuck barrier P2 Z coordinate)
SECT_USPARA_BARRIER_X3	X (chuck barrier P3 X coordinate)
SECT_USPARA_BARRIER_Z3	Z (chuck barrier P 3 Z coordinate)
SECT_USPARA_TAIL_X1	X (tailstock barrier P1 X coordinate)
SECT_USPARA_TAIL_Z1	Z (tailstock barrier P1 Z coordinate)
SECT_USPARA_TAIL_X2	X (tailstock barrier P2 X coordinate)
SECT_USPARA_TAIL_Z2	Z (tailstock barrier P2 Z coordinate)
SECT_USPARA_TAIL_X3	X (tailstock barrier P3 X coordinate)
SECT_USPARA_TAIL_Z3	Z (tailstock barrier P3 Z coordinate)
SECT_USPARA_DATA_00	Data input port No.
SECT_USPARA_DATA_01	Data input device No.
SECT_USPARA_DATA_02	Data output port No.
SECT_USPARA_DATA_03	Data output device No.

SECT_USPARA_NCRUN_00	NC operation port No.
SECT_USPARA_NCRUN_01	NC operation device No.
SECT_USPARA_MACRO_01	Macro print port No.
SECT_USPARA_MACRO_02	Macro printing device No.
SECT_USPARA_DEV1_00	Equipment 1 Name
SECT_USPARA_DEV1_01	Device 1 transfer rate
SECT_USPARA_DEV1_02	Apparatus 1 stop bit
SECT_USPARA_DEV1_03	Device 1 Parity Valid
SECT_USPARA_DEV1_04	Device 1 even parity
SECT_USPARA_DEV1_05	Apparatus 1 character length
SECT_USPARA_DEV1_06	Apparatus 1 Terminator type
SECT_USPARA_DEV1_07	Device 1 Terminator code 1
SECT_USPARA_DEV1_08	Device 1 Terminator code 2
SECT_USPARA_DEV1_09	Apparatus 1 Handshake method
SECT_USPARA_DEV1_10	Device 1 DC code parity
SECT_USPARA_DEV1_11	Device 1 DC2 / DC4 output
SECT_USPARA_DEV1_12	Apparatus 1 CR output
SECT_USPARA_DEV1_13	Apparatus 1 EIA output
SECT_USPARA_DEV1_14	Device 1 parity V
SECT_USPARA_DEV1_15	Device 1 timeout time
SECT_USPARA_DEV1_16	Device 1 EIA code 1 [
SECT_USPARA_DEV1_17	Device 1 EIA code 2]
SECT_USPARA_DEV1_18	Device 1 EIA code 3 #
SECT_USPARA_DEV1_19	Device 1 EIA code 4 *
SECT_USPARA_DEV1_20	Device 1 EIA code 5 =
SECT_USPARA_DEV1_21	Device 1 EIA code 6:
SECT_USPARA_DEV1_22	Apparatus 1 Printer type
SECT_USPARA_DEV1_23	Equipment 1 Feed number
SECT_USPARA_DEV1_24	Apparatus 1 Rewind code
SECT_USPARA_DEV2_00	Equipment 2 Name
SECT_USPARA_DEV2_01	Device 2 transfer rate
SECT_USPARA_DEV2_02	Apparatus 2 stop bit
SECT_USPARA_DEV2_03	Device 2 Parity valid

SECT_USPARA_DEV2_04	Device 2 even parity
SECT_USPARA_DEV2_05	Device 2 character length
SECT_USPARA_DEV2_06	Apparatus 2 Terminator type
SECT_USPARA_DEV2_07	Device 2 Terminator code 1
SECT_USPARA_DEV2_08	Device 2 Terminator code 2
SECT_USPARA_DEV2_09	Apparatus 2 Handshake method
SECT_USPARA_DEV2_10	Device 2 DC code parity
SECT_USPARA_DEV2_11	Device 2 DC 2 / DC 4 output
SECT_USPARA_DEV2_12	Apparatus 2 CR output
SECT_USPARA_DEV2_13	Device 2 EIA output
SECT_USPARA_DEV2_14	Device 2 Parity V
SECT_USPARA_DEV2_15	Device 2 timeout time
SECT_USPARA_DEV2_16	Device 2 EIA code 1 [
SECT_USPARA_DEV2_17	Device 2 EIA code 2]
SECT_USPARA_DEV2_18	Device 2 EIA code 3 #
SECT_USPARA_DEV2_19	Device 2 EIA code 4 *
SECT_USPARA_DEV2_20	Device 2 EIA code 5 =
SECT_USPARA_DEV2_21	Device 2 EIA code 6:
SECT_USPARA_DEV2_22	Apparatus 2 Printer type
SECT_USPARA_DEV2_23	Device 2 Feed number
SECT_USPARA_DEV2_24	Device 2 rewind code
SECT_USPARA_DEV3_00	Device 3 Name
SECT_USPARA_DEV3_01	Device 3 transfer rate
SECT_USPARA_DEV3_02	Apparatus 3 stop bit
SECT_USPARA_DEV3_03	Device 3 Parity valid
SECT_USPARA_DEV3_04	Device 3 even parity
SECT_USPARA_DEV3_05	Device 3 character length
SECT_USPARA_DEV3_06	Apparatus 3 Terminator type
SECT_USPARA_DEV3_07	Device 3 Terminator code 1
SECT_USPARA_DEV3_08	Device 3 Terminator code 2
SECT_USPARA_DEV3_09	Apparatus 3 Handshake method
SECT_USPARA_DEV3_10	Device 3 DC code parity
SECT_USPARA_DEV3_11	Device 3 DC2 / DC4 output

SECT_USPARA_DEV3_12	Device 3 CR output
SECT_USPARA_DEV3_13	Device 3 EIA output
SECT_USPARA_DEV3_14	Device 3 Parity V
SECT_USPARA_DEV3_15	Device 3 Timeout time
SECT_USPARA_DEV3_16	Device 3 EIA code 1 [
SECT_USPARA_DEV3_17	Device 3 EIA code 2]
SECT_USPARA_DEV3_18	Device 3 EIA code 3 #
SECT_USPARA_DEV3_19	Device 3 EIA code 4 *
SECT_USPARA_DEV3_20	Device 3 EIA code 5 =
SECT_USPARA_DEV3_21	Device 3 EIA code 6:
SECT_USPARA_DEV3_22	Device 3 printer type
SECT_USPARA_DEV3_23	Equipment 3 Feed number
SECT_USPARA_DEV3_24	Device 3 rewind code
SECT_USPARA_DEV4_00	Equipment 4 Name
SECT_USPARA_DEV4_01	Device 4 Transfer speed
SECT_USPARA_DEV4_02	Equipment 4 stop bits
SECT_USPARA_DEV4_03	Device 4 Parity valid
SECT_USPARA_DEV4_04	Device 4 even parity
SECT_USPARA_DEV4_05	Device 4 character length
SECT_USPARA_DEV4_06	Apparatus 4 Terminator type
SECT_USPARA_DEV4_07	Device 4 Terminator code 1
SECT_USPARA_DEV4_08	Device 4 Terminator code 2
SECT_USPARA_DEV4_09	Apparatus 4 Handshake method
SECT_USPARA_DEV4_10	Device 4 DC code parity
SECT_USPARA_DEV4_11	Device 4 DC 2 / DC 4 output
SECT_USPARA_DEV4_12	Apparatus 4 CR Output
SECT_USPARA_DEV4_13	Equipment 4 EIA output
SECT_USPARA_DEV4_14	Device 4 Parity V
SECT_USPARA_DEV4_15	Device 4 Timeout time
SECT_USPARA_DEV4_16	Device 4 EIA code 1 [
SECT_USPARA_DEV4_17	Device 4 EIA code 2]
SECT_USPARA_DEV4_18	Device 4 EIA code 3 #
SECT_USPARA_DEV4_19	Device 4 EIA code 4 *

SECT_USPARA_DEV4_20	Device 4 EIA code 5 =
SECT_USPARA_DEV4_21	Device 4 EIA code 6:
SECT_USPARA_DEV4_22	Apparatus 4 Printer type
SECT_USPARA_DEV4_23	Equipment 4 Feed number
SECT_USPARA_DEV4_24	Equipment 4 Rewind code
SECT_USPARA_DEV5_00	Device 5 Name
SECT_USPARA_DEV5_01	Device 5 Transfer speed
SECT_USPARA_DEV5_02	Equipment 5 stop bits
SECT_USPARA_DEV5_03	Device 5 Parity valid
SECT_USPARA_DEV5_04	Device 5 even parity
SECT_USPARA_DEV5_05	Device 5 character length
SECT_USPARA_DEV5_06	Apparatus 5 Terminator type
SECT_USPARA_DEV5_07	Device 5 Terminator code 1
SECT_USPARA_DEV5_08	Device 5 Terminator code 2
SECT_USPARA_DEV5_09	Apparatus 5 Handshake method
SECT_USPARA_DEV5_10	Device 5 DC code parity
SECT_USPARA_DEV5_11	Device 5 DC2 / DC4 output
SECT_USPARA_DEV5_12	Device 5 CR output
SECT_USPARA_DEV5_13	Device 5 EIA output
SECT_USPARA_DEV5_14	Device 5 Parity V
SECT_USPARA_DEV5_15	Device 5 timeout time
SECT_USPARA_DEV5_16	Device 5 EIA code 1 [
SECT_USPARA_DEV5_17	Device 5 EIA code 2]
SECT_USPARA_DEV5_18	Device 5 EIA code 3 #
SECT_USPARA_DEV5_19	Device 5 EIA code 4 *
SECT_USPARA_DEV5_20	Device 5 EIA code 5 =
SECT_USPARA_DEV5_21	Device 5 EIA code 6:
SECT_USPARA_DEV5_22	Apparatus 5 Printer type
SECT_USPARA_DEV5_23	Equipment 5 Feed number
SECT_USPARA_DEV5_24	Device 5 rewind code
SECT_MCPARA_AXNAME	axname
SECT_MCPARA_INCAX	incax
SECT_MCPARA_CUNIT	cunit

SECT_MCPARA_SP_AX	sp_ax
SECT_MCPARA_INOUT	iout
SECT_MCPARA_ROT	rot
SECT_MCPARA_CCW	ccw
SECT_MCPARA_SVOF	svof
SECT_MCPARA_DIA	dia
SECT_MCPARA_POLAR	polar
SECT_MCPARA_ABSON	abson
SECT_MCPARA_INTABS	intabs
SECT_MCPARA_AXNAME2	axname2
SECT_MCPARA_CROS_$	cros_$
SECT_MCPARA_CROS_SYS	cros_$
SECT_MCPARA_AXOFF	axoff
SECT_MCPARA_MCP_NO	mcp_no
SECT_MCPARA_REGLTRQ	regltrq
SECT_MCPARA_PROPTRQ	proptrq
SECT_MCPARA_MFIG	Mfig
SECT_MCPARA_MBIN	Mbin
SECT_MCPARA_SFIG	Sfig
SECT_MCPARA_SBIN	Sbin
SECT_MCPARA_TFIG	Tfig
SECT_MCPARA_TBIN	Tbin
SECT_MCPARA_M2FIG	M2fig
SECT_MCPARA_M2BIN	M2bin
SECT_MCPARA_M2NAME	M2name
SECT_MCPARA_SKIP_F	skip_F
SECT_MCPARA_SKIP_C	skip_C
SECT_MCPARA_EXTDCC	extdcc
SECT_MCPARA_TAPOVR	tapovr
SECT_MCPARA_THR_SF	thr_SF
SECT_MCPARA_TAP_TL	tap_tl
SECT_MCPARA_DWLSKP	dwlskp
SECT_MCPARA_G96_AX	G96_ax

SECT_MCPARA_CLMP_M	clmp_M
SECT_MCPARA_CLMP_D	clmp_D
SECT_MCPARA_ORIGIN	origin
SECT_MCPARA_TLMSENS	tlmsens
SECT_MCPARA_MIROFS	mirofs
SECT_MCPARA_TMIRS1	TmirS1
SECT_MCPARA_TMIRS2	TmirS2
SECT_MCPARA_MILL_AX	mill_ax
SECT_MCPARA_MILL_C	mill_C
SECT_MCPARA_SBS_NO	SBS_no
SECT_MCPARA_SBS_PRO	SBS_pro
SECT_MCPARA_SSELECT	Sselect
SECT_MCPARA_ADR_ABS_1	adr_abs[1]
SECT_MCPARA_ADR_ABS_2	adr_abs[2]
SECT_MCPARA_ADR_ABS_3	adr_abs[3]
SECT_MCPARA_ADR_ABS_4	adr_abs[4]
SECT_MCPARA_ADR_ABS_5	adr_abs[5]
SECT_MCPARA_ADR_ABS_6	adr_abs[6]
SECT_MCPARA_ADR_ABS_7	adr_abs[7]
SECT_MCPARA_ADR_ABS_8	adr_abs[8]
SECT_MCPARA_ADR_INC_1	adr_inc[1]
SECT_MCPARA_ADR_INC_2	adr_inc[2]
SECT_MCPARA_ADR_INC_3	adr_inc[3]
SECT_MCPARA_ADR_INC_4	adr_inc[4]
SECT_MCPARA_ADR_INC_5	adr_inc[5]
SECT_MCPARA_ADR_INC_6	adr_inc[6]
SECT_MCPARA_ADR_INC_7	adr_inc[7]
SECT_MCPARA_ADR_INC_8	adr_inc[8]
SECT_MCPARA_REAL_I	real_I
SECT_MCPARA_REAL_J	real_J
SECT_MCPARA_REAL_K	real_K
SECT_MCPARA_RLIJREV	rlIJrev
SECT_MCPARA_RLKIREV	rlKIrev

SECT_MCPARA_RLJKREV	rlJKrev
SECT_MCPARA_S_XCNT	s_xcnt
SECT_MCPARA_S_AX1	s_ax1
SECT_MCPARA_S_AX2	s_ax2
SECT_MCPARA_PROAXY	proaxy
SECT_MCPARA_MACAXX	macaxx
SECT_MCPARA_MACAXY	macaxy
SECT_MCPARA_IMAG_AX1	imag_ax[1]
SECT_MCPARA_IMAG_AX2	imag_ax[2]
SECT_MCPARA_IMGREV	imgrev
SECT_MCPARA_IMGAX1_OT_MINUS	imgax1 OT-
SECT_MCPARA_IMGAX1_OT_PLUS	imgax1 OT+
SECT_MCPARA_IMGAX2_OT_MINUS	imgax2 OT-
SECT_MCPARA_IMGAX2_OT_PLUS	imgax2 OT+
SECT_MCPARA_IMGAX1_OT2_MINUS	imgax1 OT2-
SECT_MCPARA_IMGAX1_OT2_PLUS	imgax1 OT2+
SECT_MCPARA_IMGAX2_OT2_MINUS	imgax2 OT2-
SECT_MCPARA_IMGAX2_OT2_PLUS	imgax2 OT2+
SECT_MCPARA_SRN_MST	SRN_MST
SECT_MCPARA_BT_AXNAME	bt_axname
SECT_MCPARA_BT_BASE	bt_base
SECT_MCPARA_BT_LX	bt_lx
SECT_MCPARA_BT_LZ	bt_lz
SECT_MCPARA_BT_POLAR	bt_polar
SECT_MCPARA_BT_PLANE	bt_plane
SECT_MCPARA_SP1INI	sp1ini
SECT_MCPARA_SP2INI	sp2ini
SECT_MCPARA_ABS_POSITION	ABS POSITION
SECT_MCPARA_SET	SET
SECT_MCPARA_BASE	base
SECT_MCPARA_SLIP	slip
SECT_MCPARA_G28MAX	G28max
SECT_MCPARA_NO_STOPPER	no stopper

SECT_MCPARA_ABS_ILP	abs.ILP±%
SECT_MCPARA_OD	OD
SECT_MCPARA_REF_PNT_TYPE	ref.pnt.typ
SECT_MCPARA_APPROACH	approach
SECT_MCPARA_POS_SW1_AXIS	axis
SECT_MCPARA_POS_SW1_DOG1	dog1
SECT_MCPARA_POS_SW1_DOG2	dog2